

DM74LS540

Octal Buffer/Line Driver with TRI-STATE® Outputs

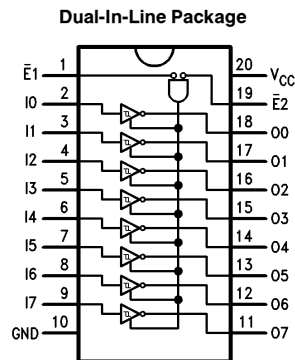
General Description

The 'LS540 is similar in function to the 'LS240, except that the inputs and outputs are on opposite sides of the package (see Connection Diagram). This pinout arrangement makes this device especially useful as an output port for microprocessors, allowing ease of layout and greater PC board density.

Features

- Hysteresis at inputs to improve noise margin
- PNP inputs reduce loading
- TRI-STATE outputs drive bus lines
- Inputs and outputs opposite side of package, allowing easier interface to microprocessors
- Fully TTL and CMOS compatible

Connection Diagram



TL/F/9813-1

Order Number DM74LS540WM or DM74LS540N
See NS Package Number M20B or N20A

Truth Table

Pin Name	Description
$\bar{E}1, \bar{E}2$	Output Enable (Active Low)
I0-7	Data Inputs
O0-7	Data Outputs

Inputs			Outputs
E1	E2	D	
L	L	H	L
H	X	X	Z
X	H	X	Z
L	L	L	H

H = HIGH Voltage Level
L = LOW Voltage Level
X = Immaterial
Z = High Impedance

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Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage	7V
Input Voltage	7V
Operating Free Air Temperature	0°C to +70°C
DM74LS	
Storage Temperature Range	−65°C to +150°C

Note: The “Absolute Maximum Ratings” are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the “Electrical Characteristics” table are not guaranteed at the absolute maximum ratings. The “Recommended Operating Conditions” table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	DM74LS540			Units
		Min	Nom	Max	
V _{CC}	Supply Voltage	4.75	5	5.25	V
V _{IH}	High Level Input Voltage	2			V
V _{IL}	Low Level Input Voltage			0.8	V
I _{OH}	High Level Output Current			−3	mA
I _{OL}	Low Level Output Current			24	mA
T _A	Free Air Operating Temperature	0		70	°C

Electrical Characteristics

Over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 1)	Max	Units
V _I	Input Clamp Voltage	V _{CC} = Min, I _I = −18 mA			−1.5	V
V _{OH}	High Level Output Voltage	V _{CC} = Min, I _{OH} = Max, V _{IL} = Max	2.7	3.4		V
V _{OL}	Low Level Output Voltage	V _{CC} = Min, I _{OL} = Max, V _{IH} = Min		0.35	0.5	V
		I _{OL} = 4 mA, V _{CC} = Min		0.25	0.4	
I _I	Input Current @ Max Input Voltage	V _{CC} = Max, V _I = 7V			0.1	mA
I _{IH}	High Level Input Current	V _{CC} = Max, V _I = 2.7V			20	μA
I _{IL}	Low Level Input Current	V _{CC} = Max, V _I = 0.4V			−0.2	mA
I _{OS}	Short Circuit Output Current	V _{CC} = Max (Note 3)	−50		−225	mA
I _{CC}	Supply Current	V _{CC} = Max			50	mA
I _{OZH}	TRI-STATE Output Off Current High	V _{CC} = Max, V _O = 2.7V			20	μA
I _{OZL}	TRI-STATE Output Off Current Low	V _{CC} = Max, V _O = 0.4V			−20	μA

Note 1: All typicals are at V_{CC} = 5V, T_A = 25°C.

Note 2: Not more than one output should be shorted at a time, and the duration should not exceed one second.

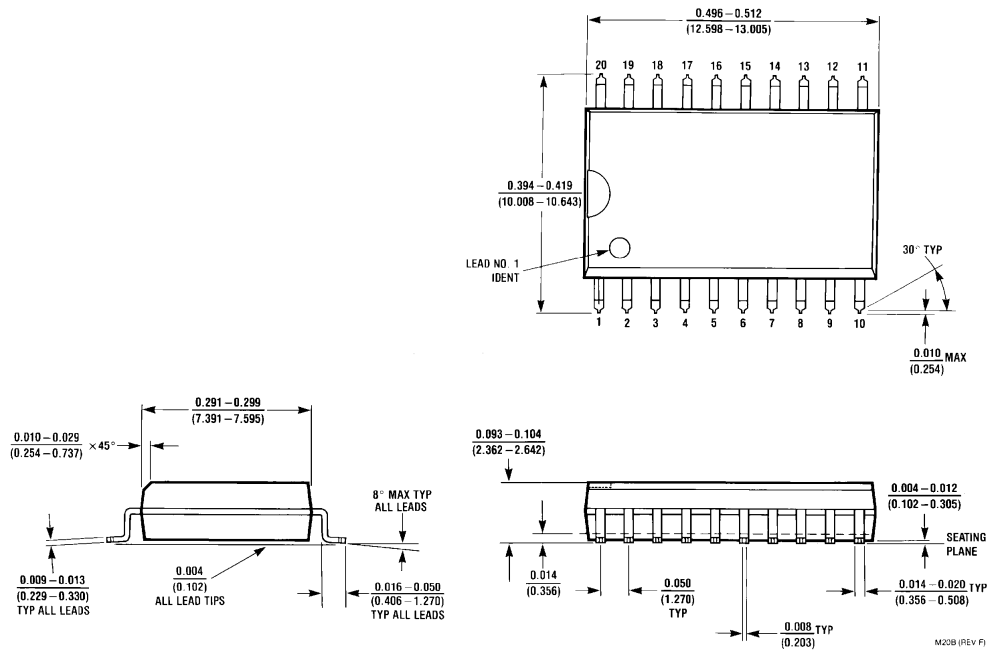
Switching Characteristics

at $V_{CC} = 5V$ and $T_A = 25^\circ C$

Symbol	Parameter	Conditions	Min	Max	Units
t_{PLH} t_{PHL}	Propagation Delay Data to Output	$C_L = 50 \text{ pF}$		14 18	ns
t_{PZH} t_{PZL}	Output Enable Time	$R_L = 667\Omega$, $C_L = 50 \text{ pF}$		23 30	ns
t_{PLZ} t_{PHZ}	Output Disable Time	$R_L = 667\Omega$, $C_L = 50 \text{ pF}$		25 18	ns

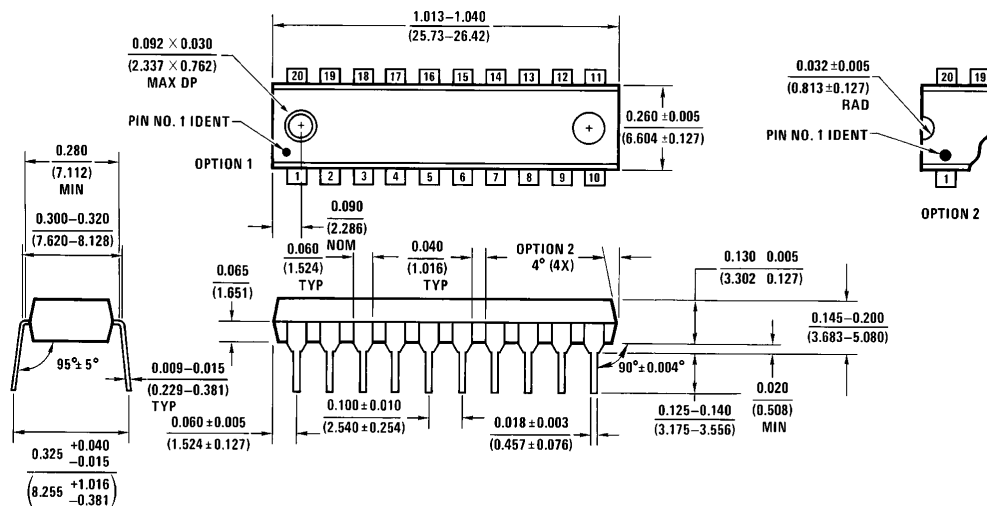
*DC limits apply over operating temperature range; AC limits apply at $T_A = +25^\circ$ and $V_{CC} = +5.0V$.

Physical Dimensions inches (millimeters)



20-Lead Wide Small Outline Molded Package (M)
Order Number DM74LS540WM
NS Package Number M20B

M20B (REV F)

Physical Dimensions inches (millimeters) (Continued)

20-Lead Molded Dual-In-Line Package (N)
Order Number DM74LS540N
NS Package Number N20A

N20A (REV G)

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