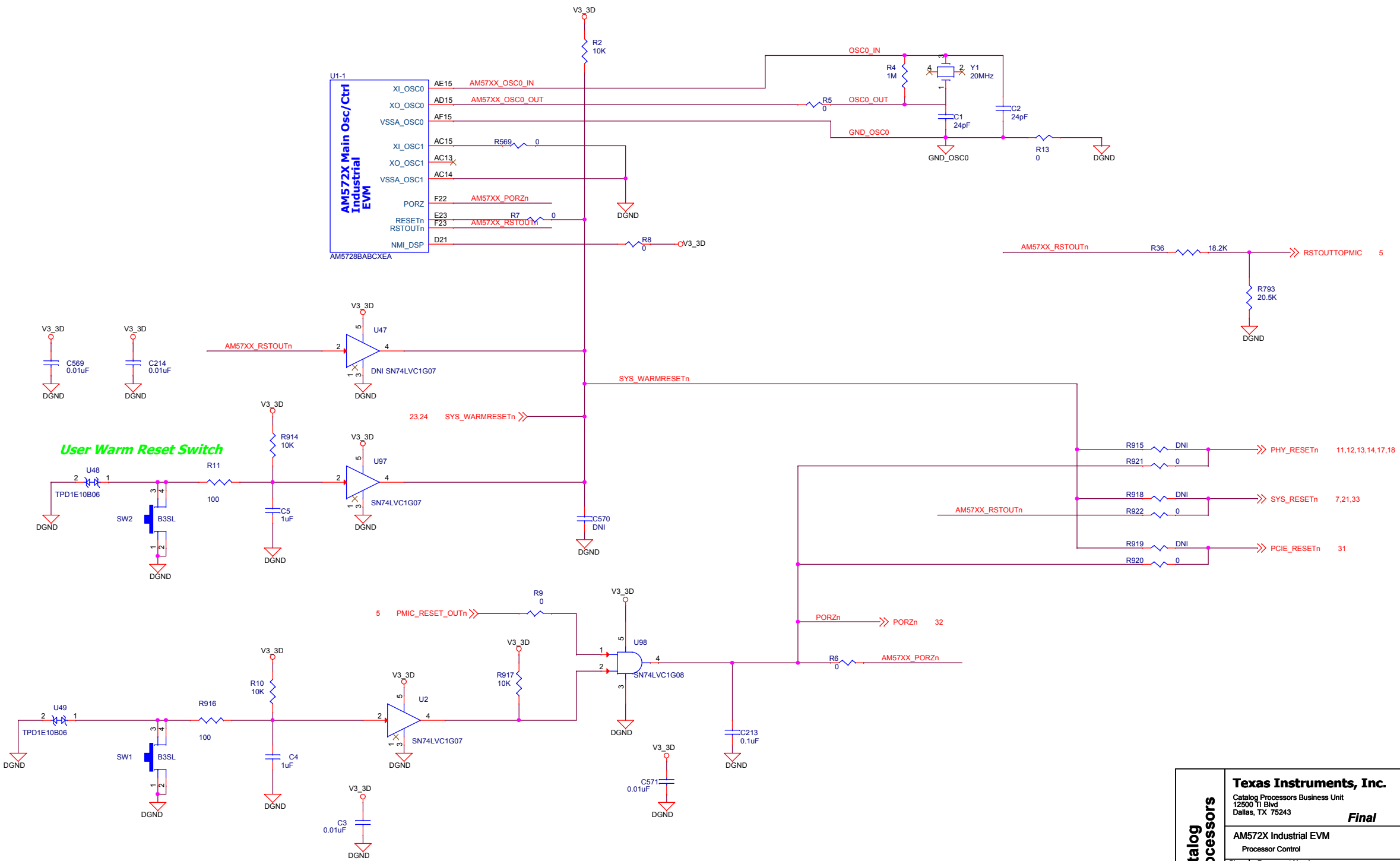


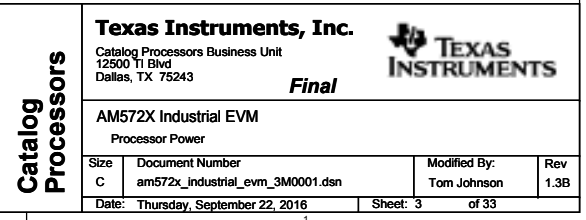
ARM MPU AM572X Industrial EVM ABC Package

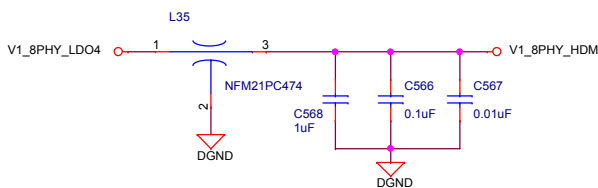
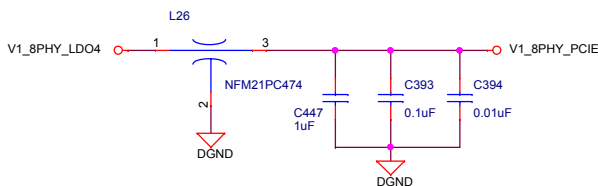
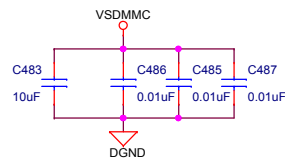
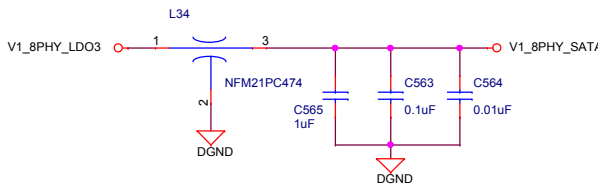
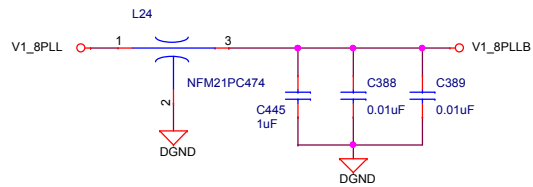
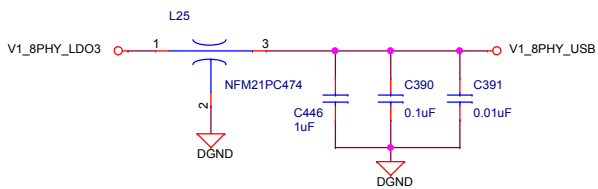
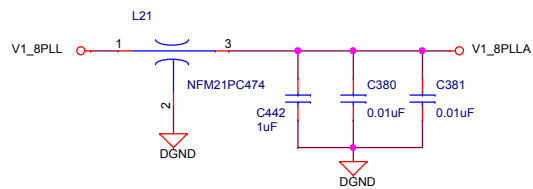
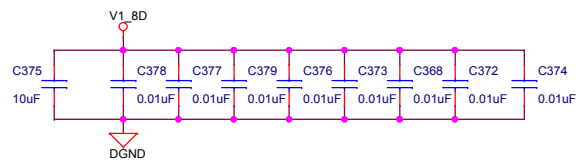
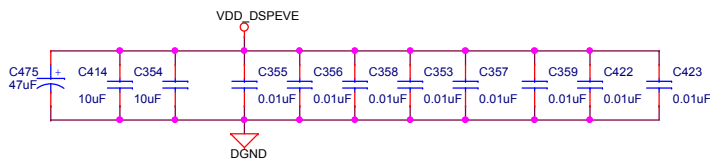
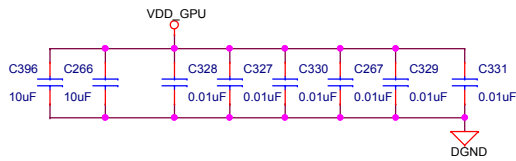
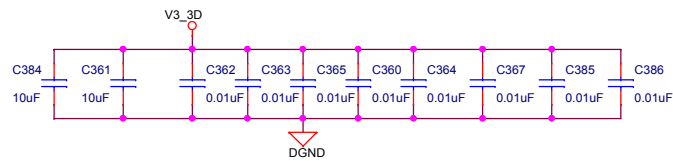
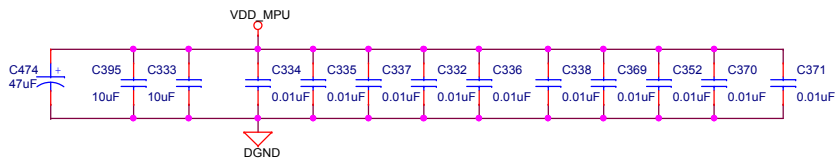
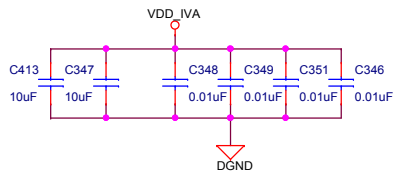
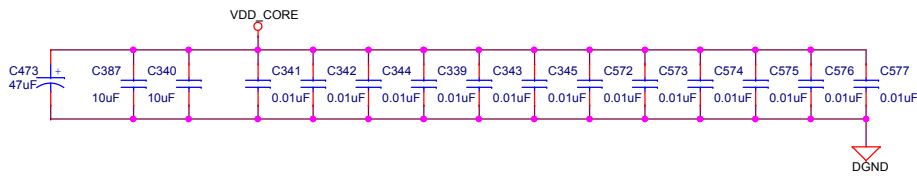
- See the Hardware User Guide for board details
- See the Errata Document for design Issues
- See the PCB Build Specification for PCB Details

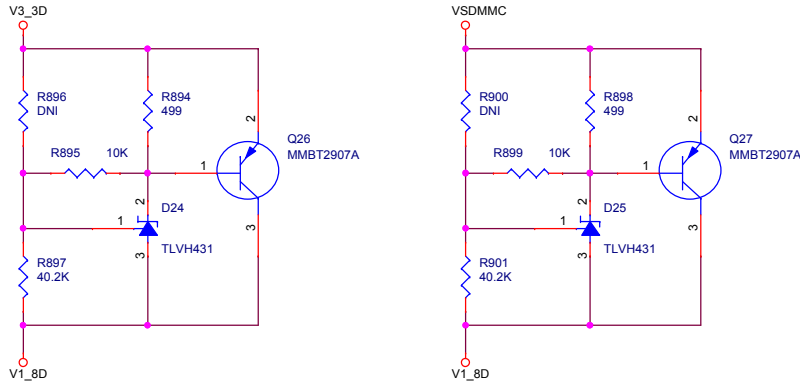
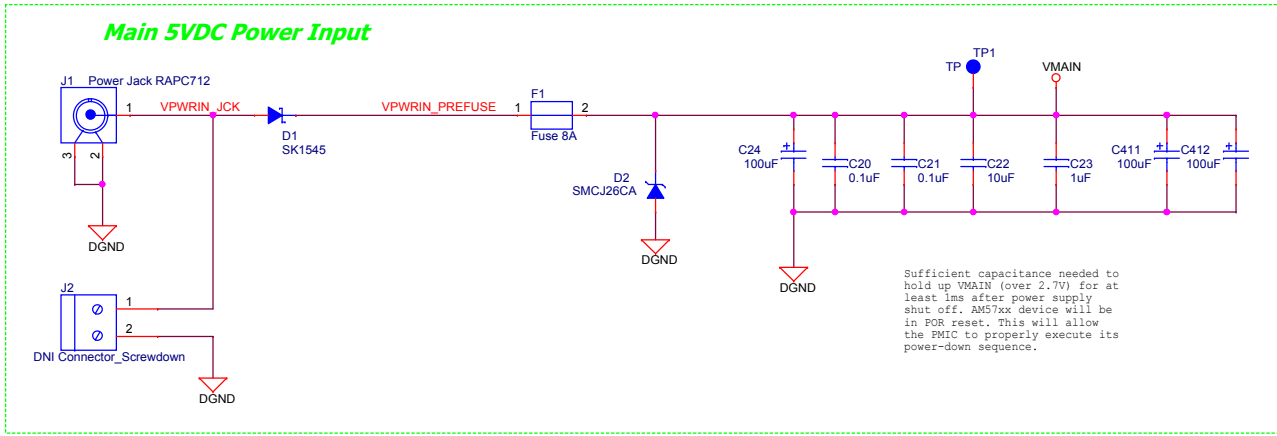
PCB1
AM572x Industrial EVM Bare PCB

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	Catalog Processors Business Unit 12500 TI Blvd Dallas, TX 75243		
	Final		
	AM572X Industrial EVM		
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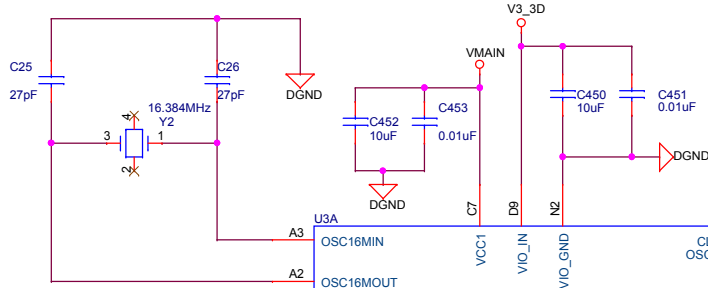




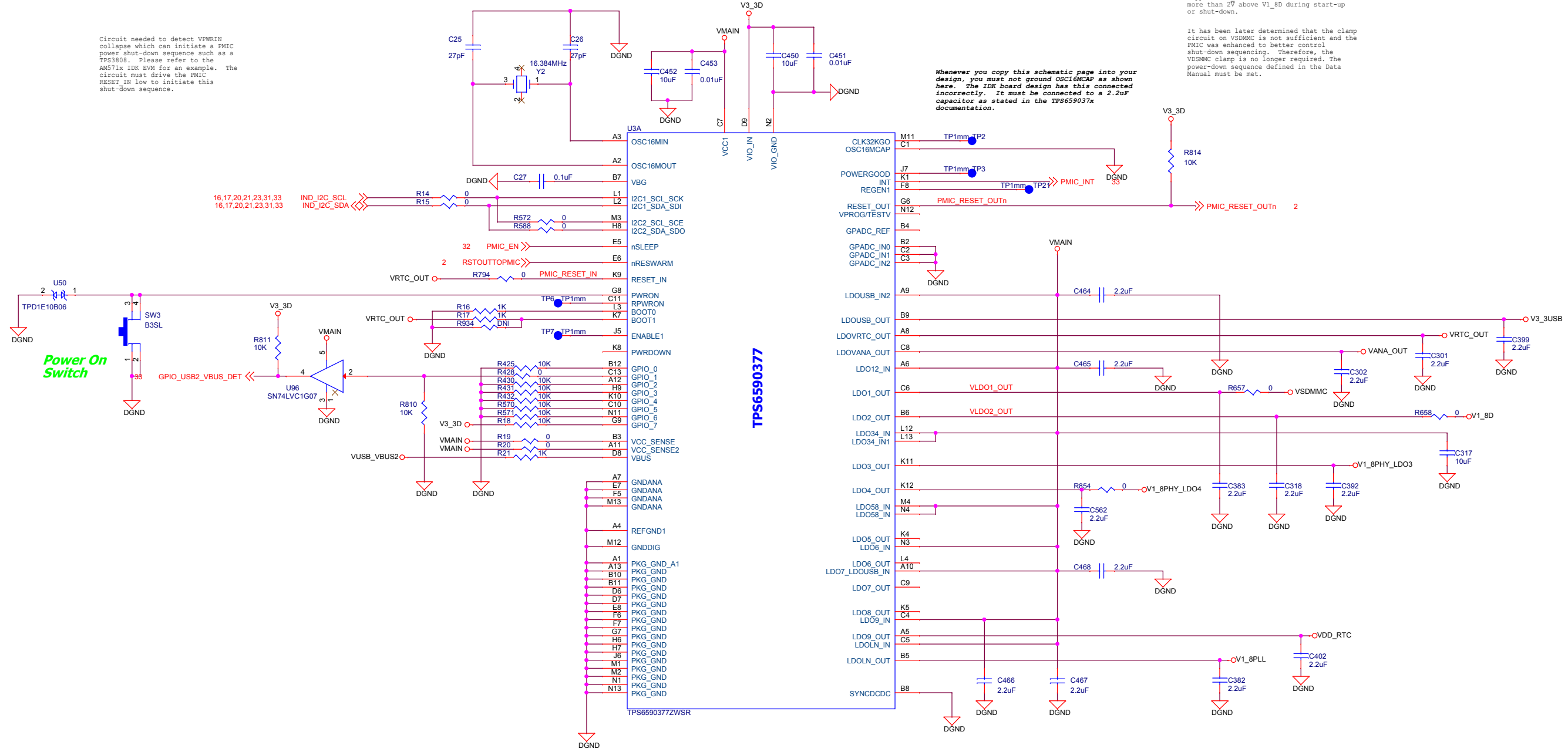
Clamp circuits added to guarantee the 3.3V supplies V3_3D and VSDMMC never become more than 2V above V1_8D during start-up or shut-down.

It has been later determined that the clamp circuit on VSDMMC is not sufficient and the PMIC was enhanced to better control shut-down sequencing. Therefore, the VSDMMC clamp is no longer required. The power-down sequence defined in the Data Manual must be met.

Circuit needed to detect VPWRIN collapse which can initiate a PMIC power shut-down sequence such as a TPS3808. Please refer to the AM571x IDK EVM for an example. The circuit must drive the PMIC RESET_IN low to initiate this shut-down sequence.



Whenever you copy this schematic page into your design, you must not ground OSC16MCP as shown here. The IDK board design has this connected incorrectly. It must be connected to a 2.2uF capacitor as stated in the TPS659037x documentation.



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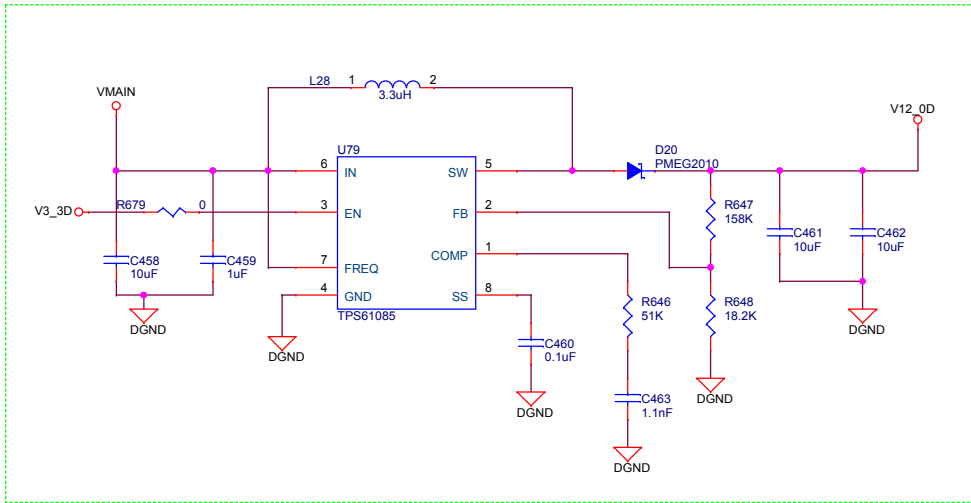
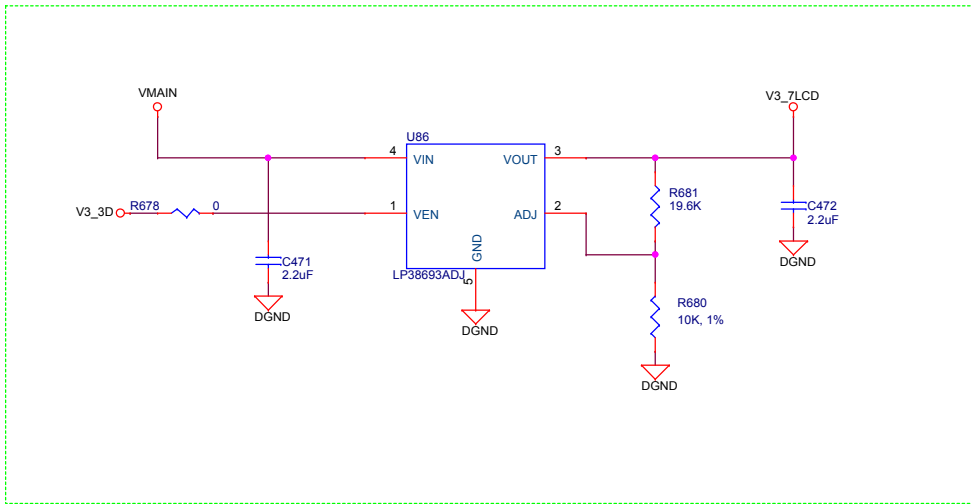
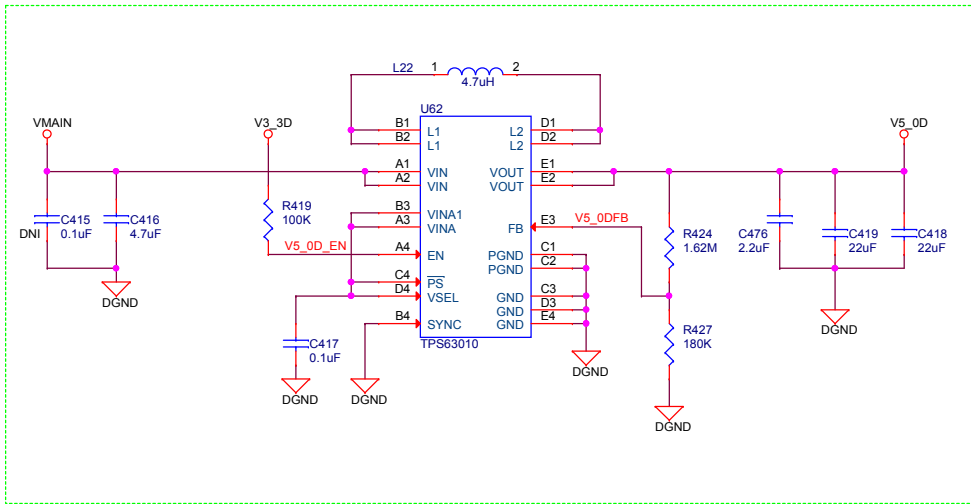
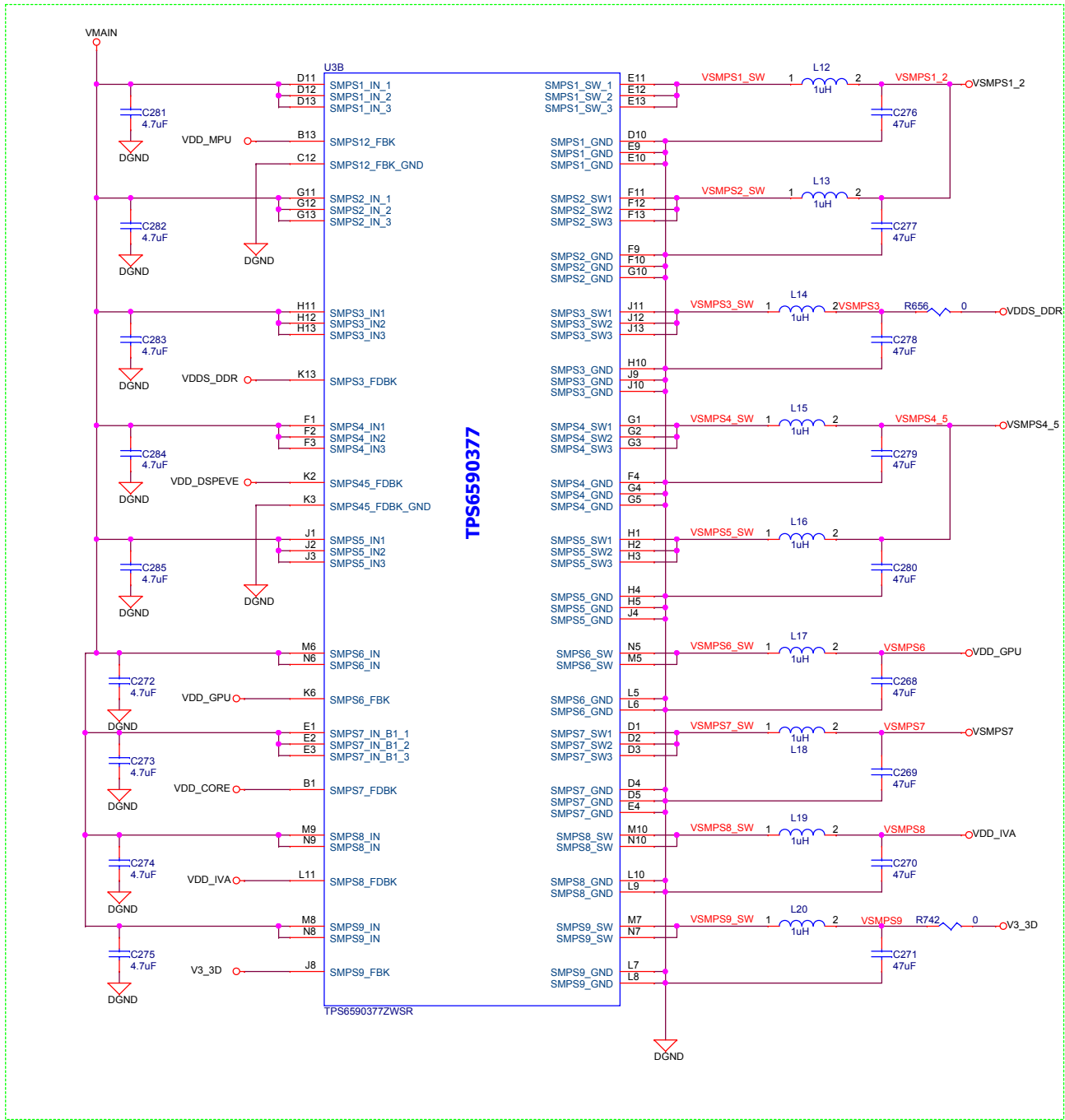
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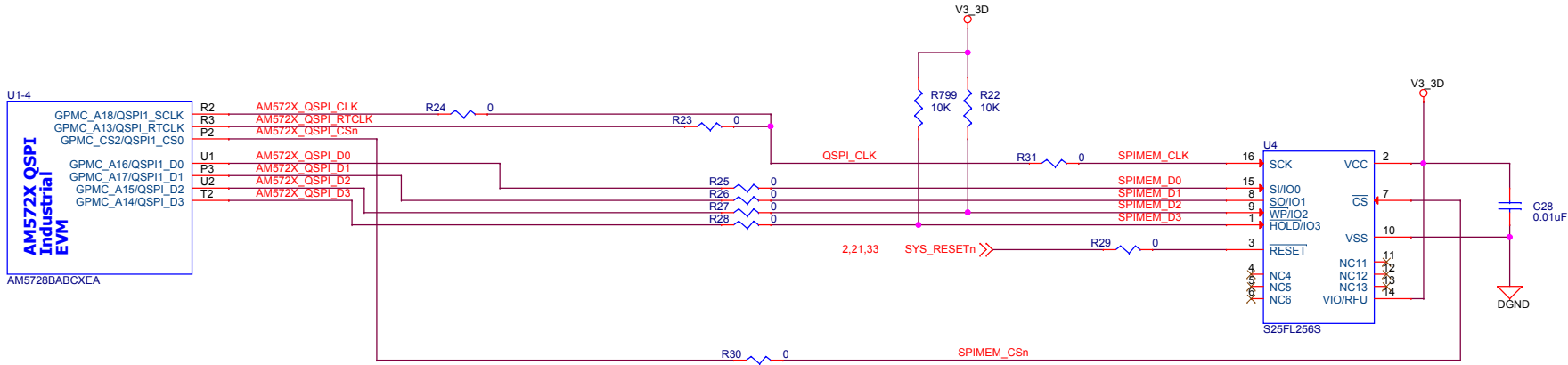
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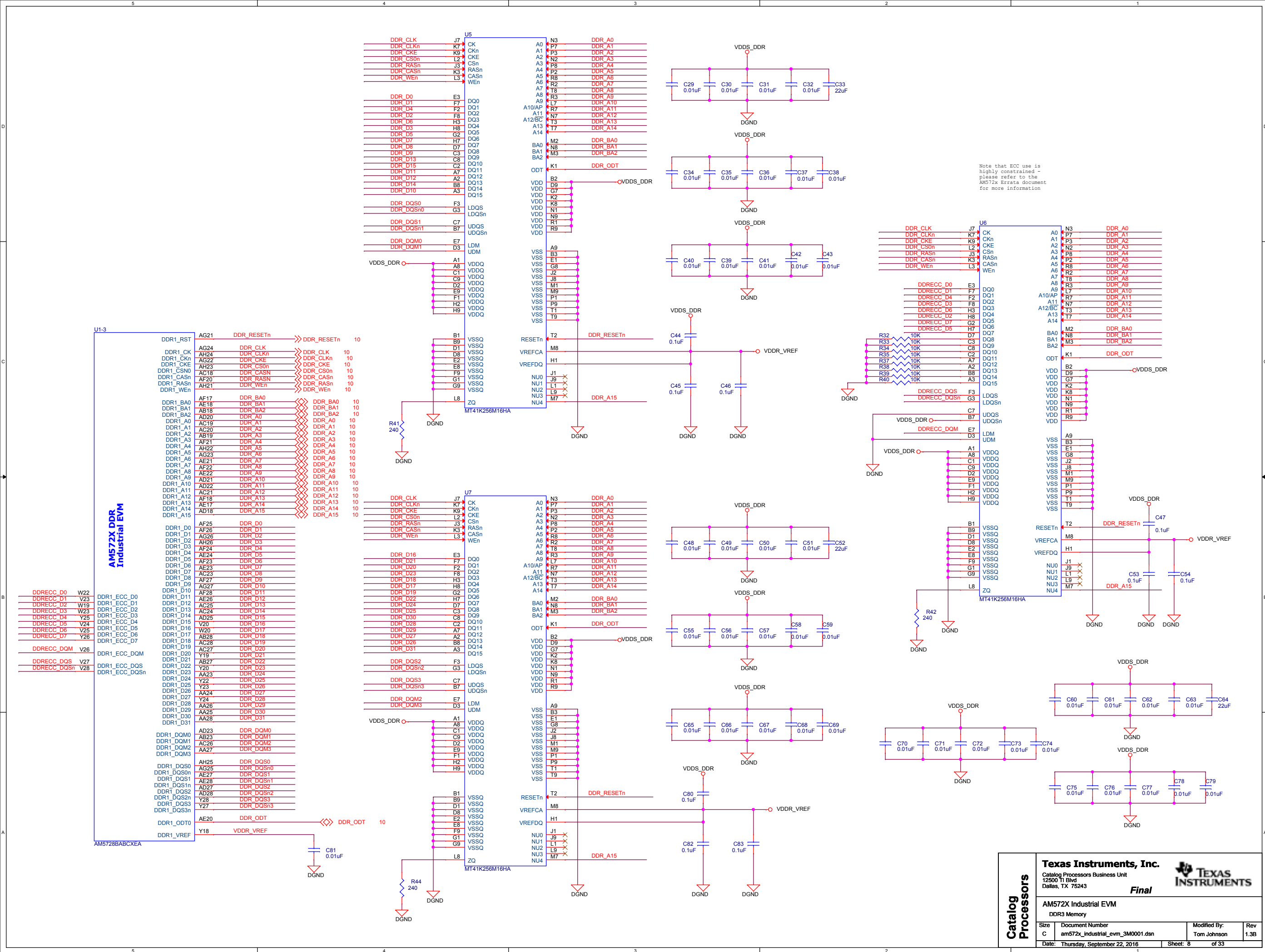
AM572X Industrial EVM

Power

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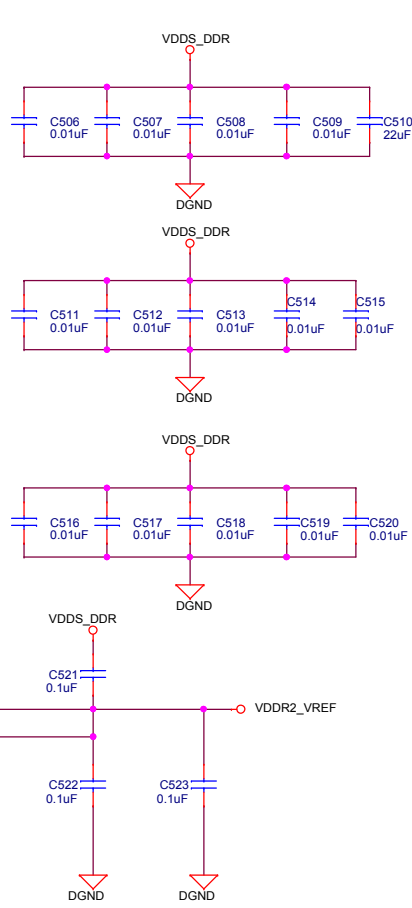
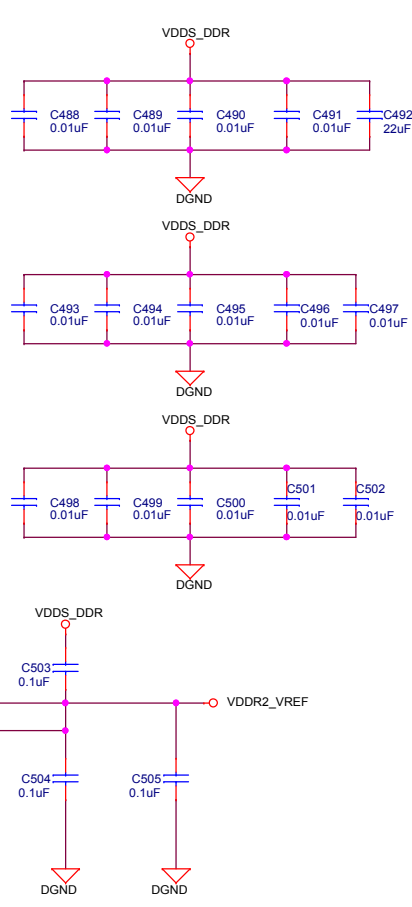
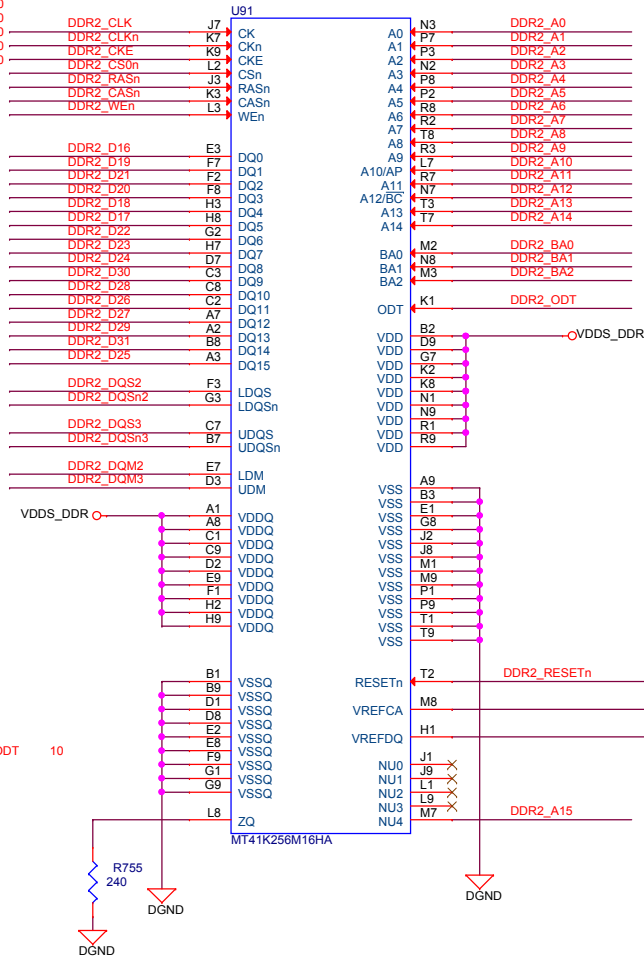
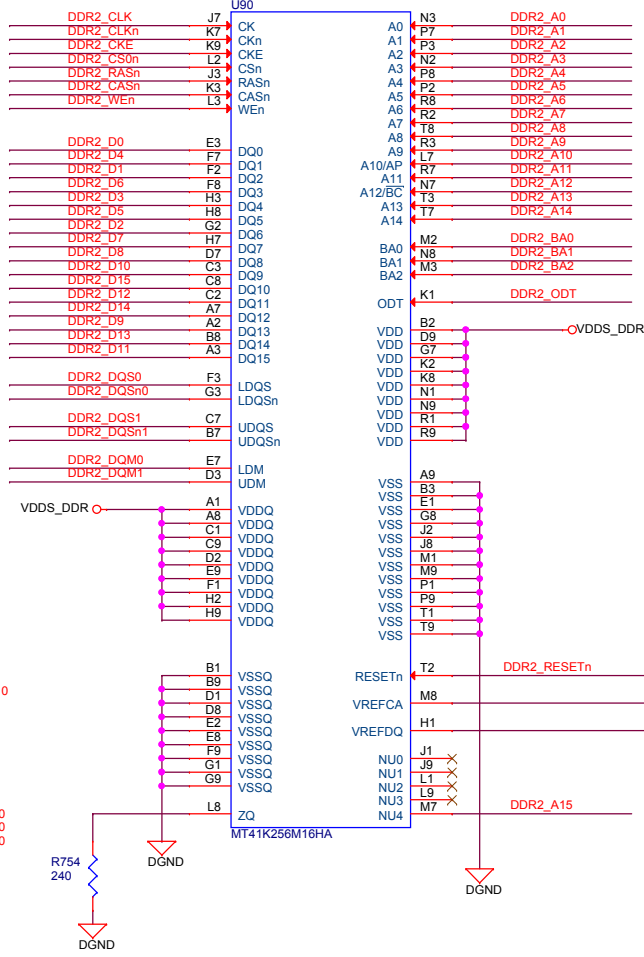
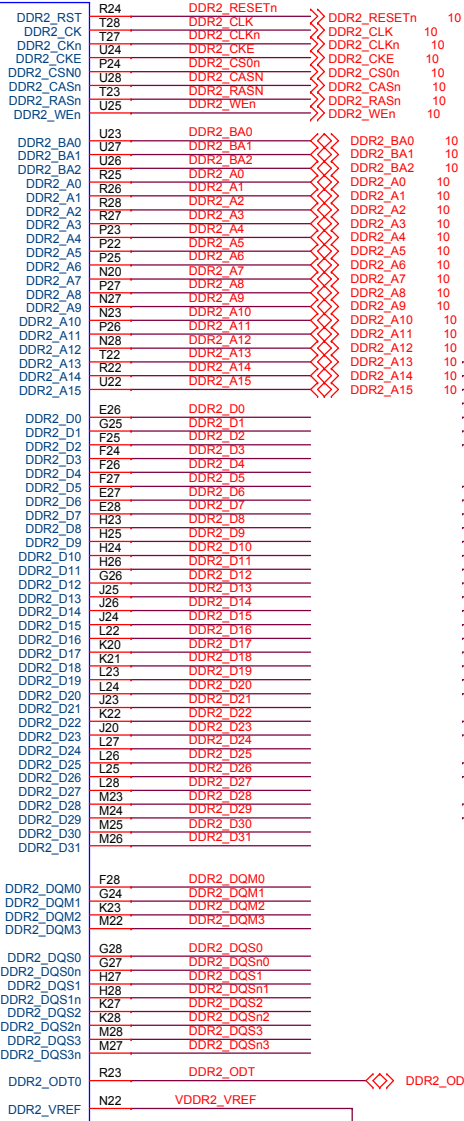


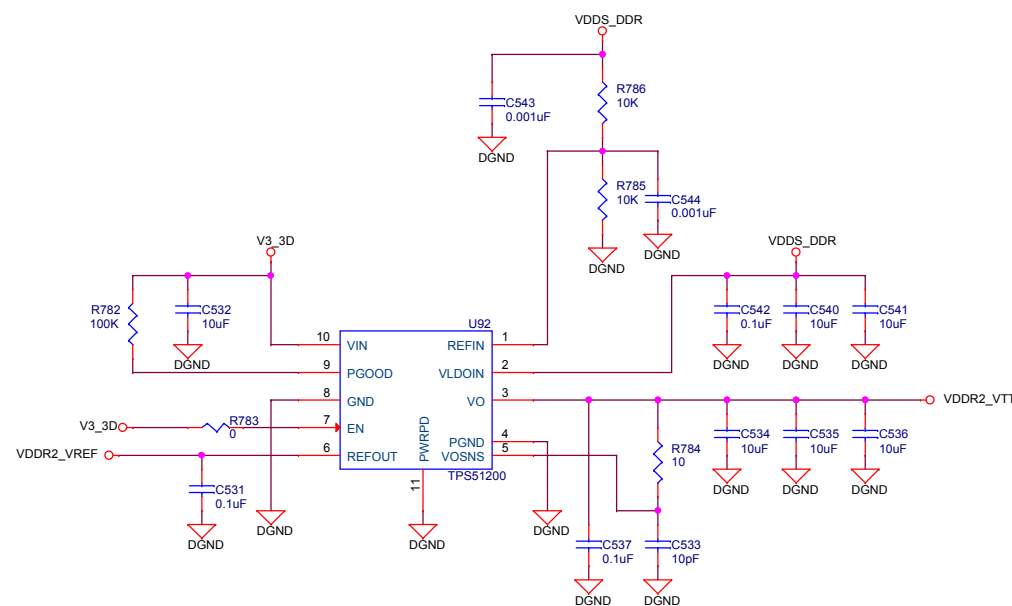
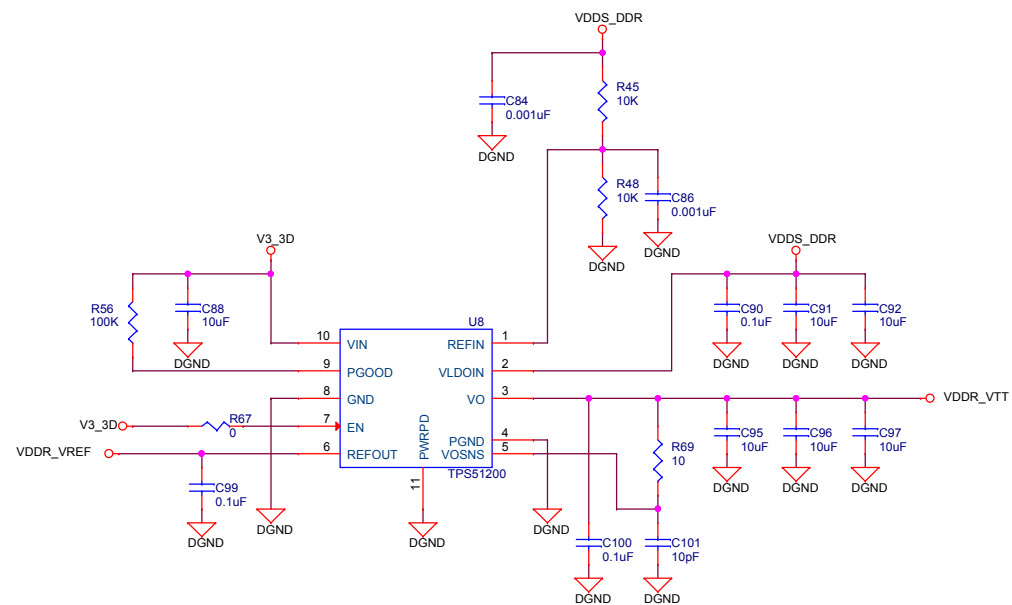
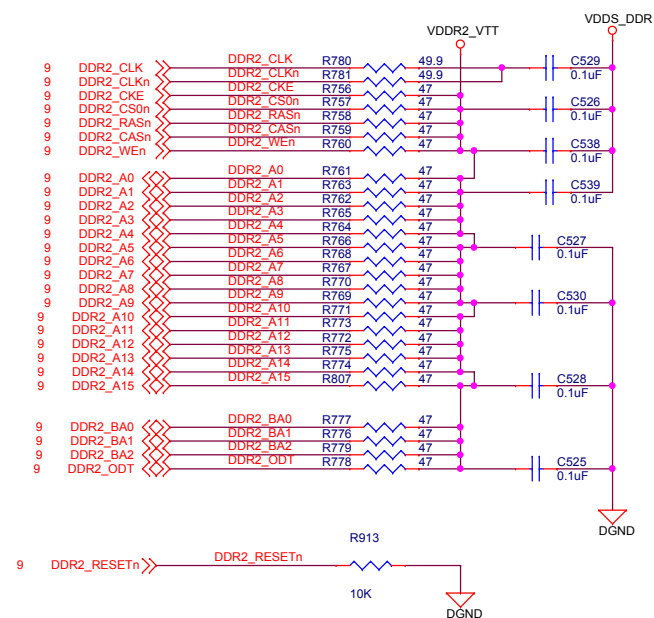
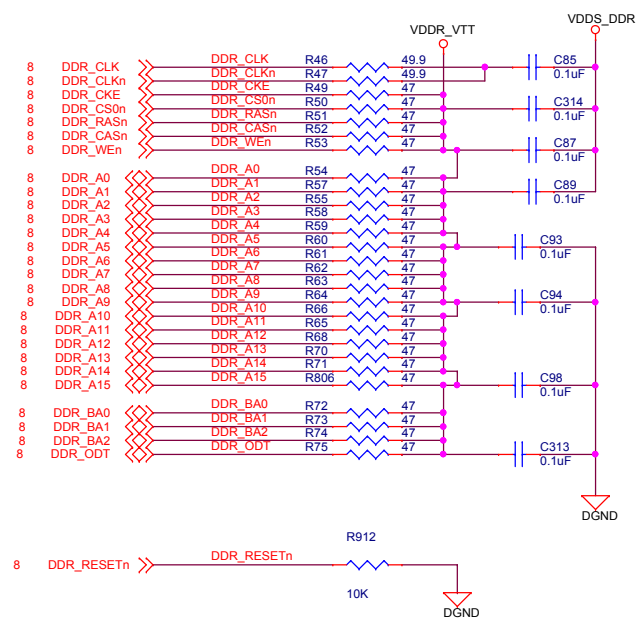


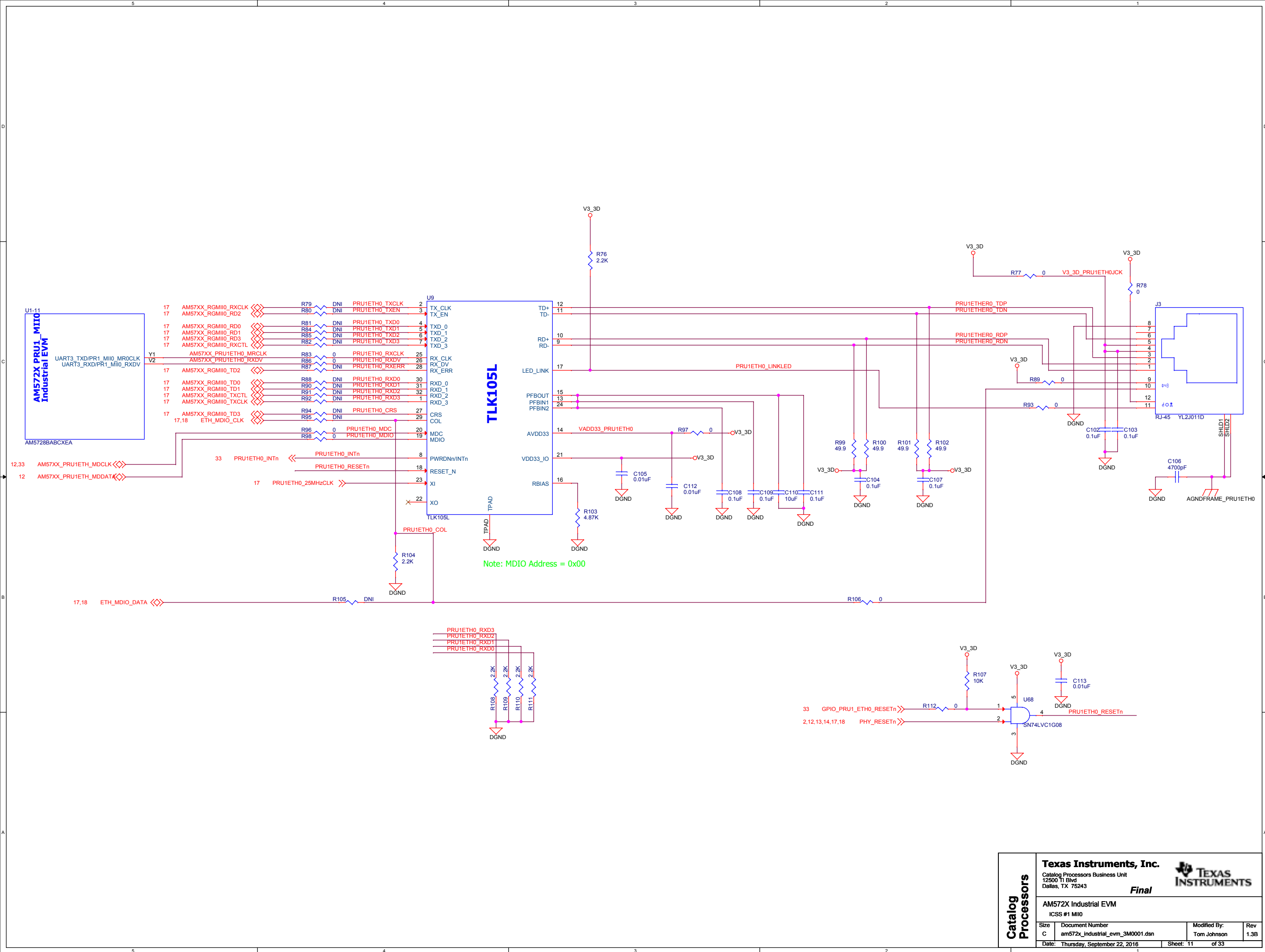


U1-26

AM572X DDR #2
Industrial EVM

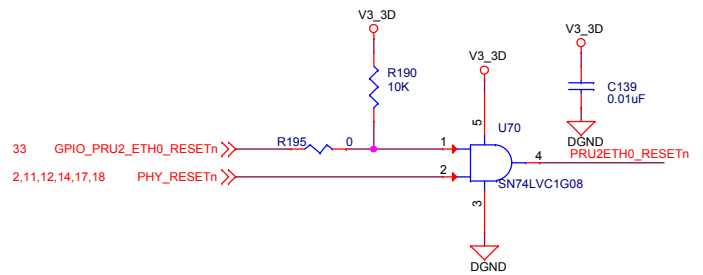
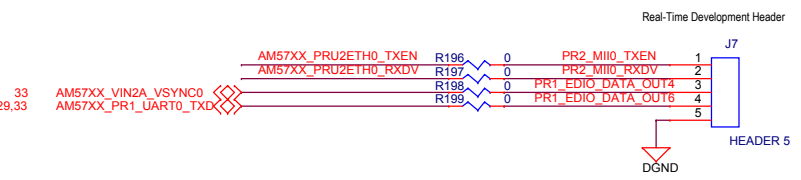
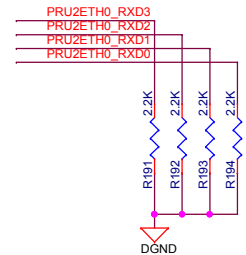
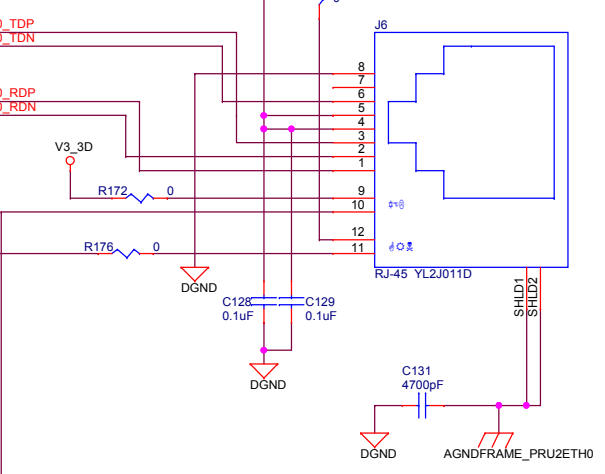
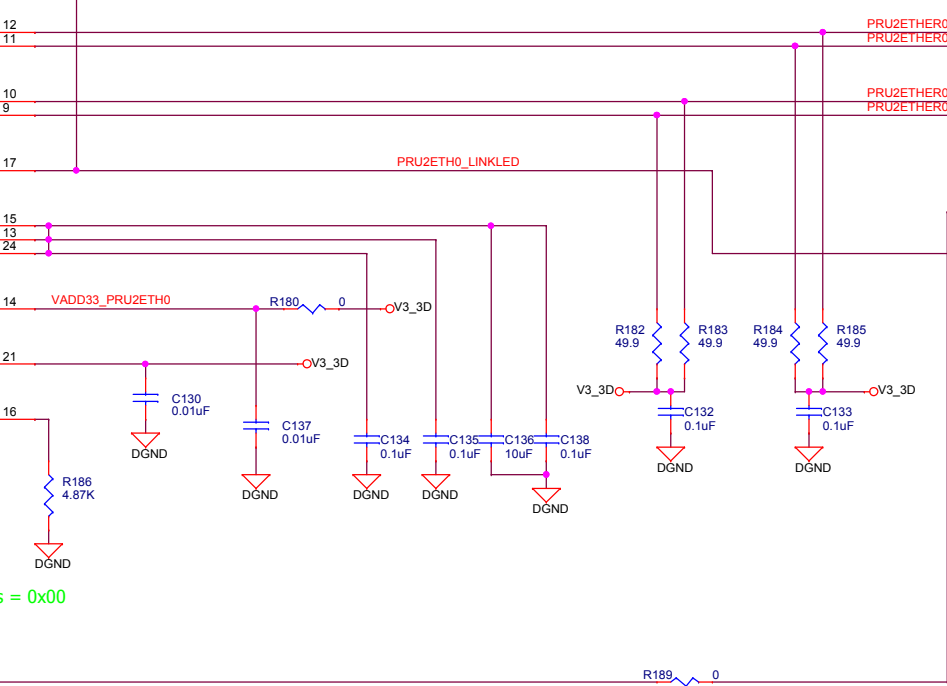
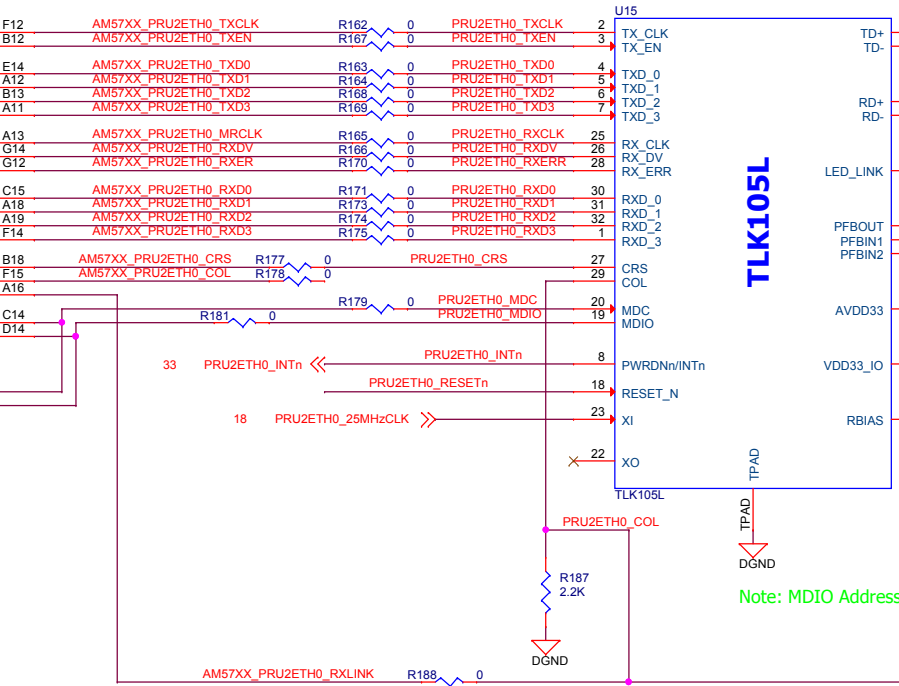








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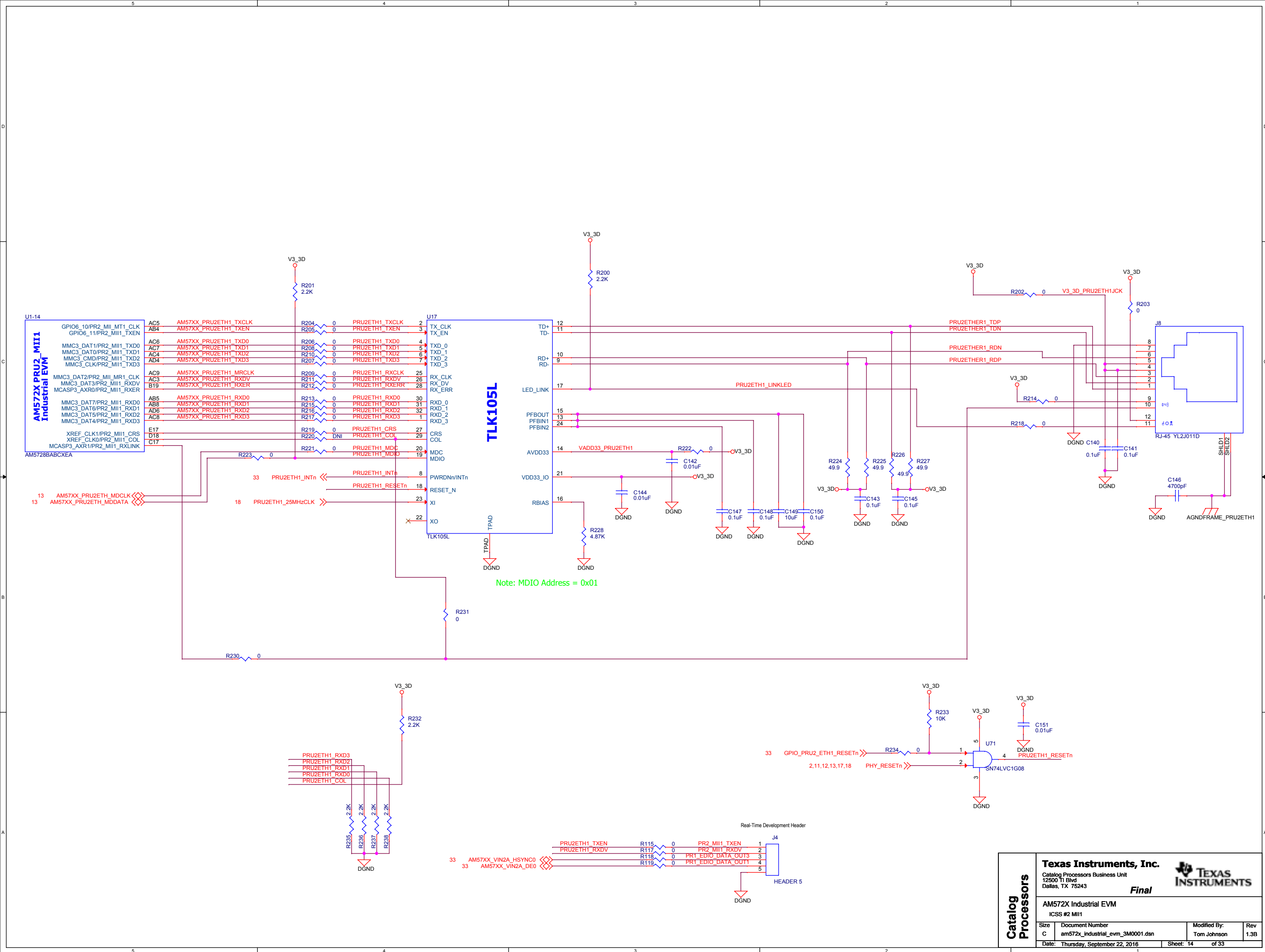
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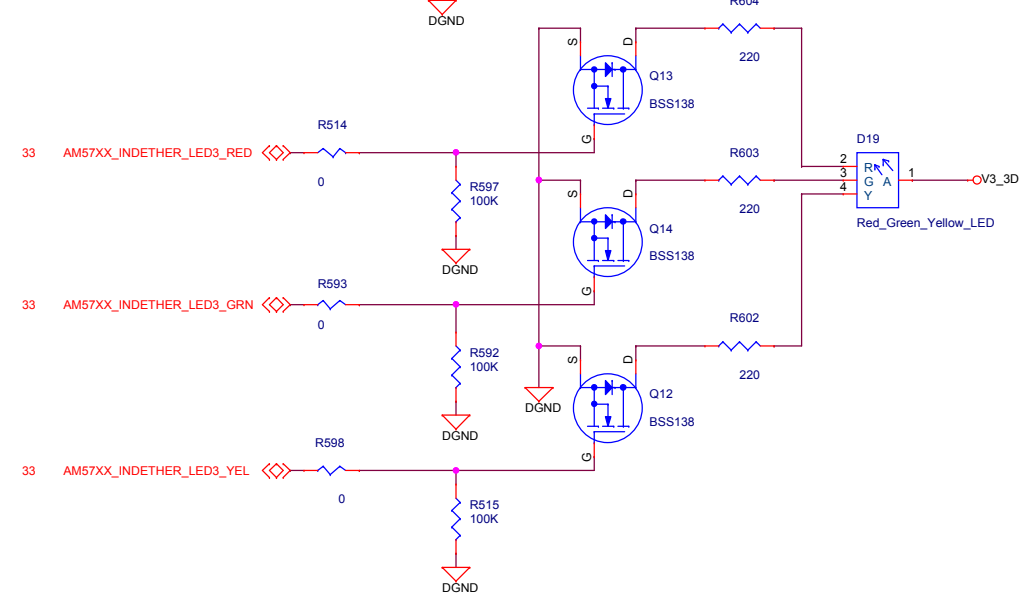
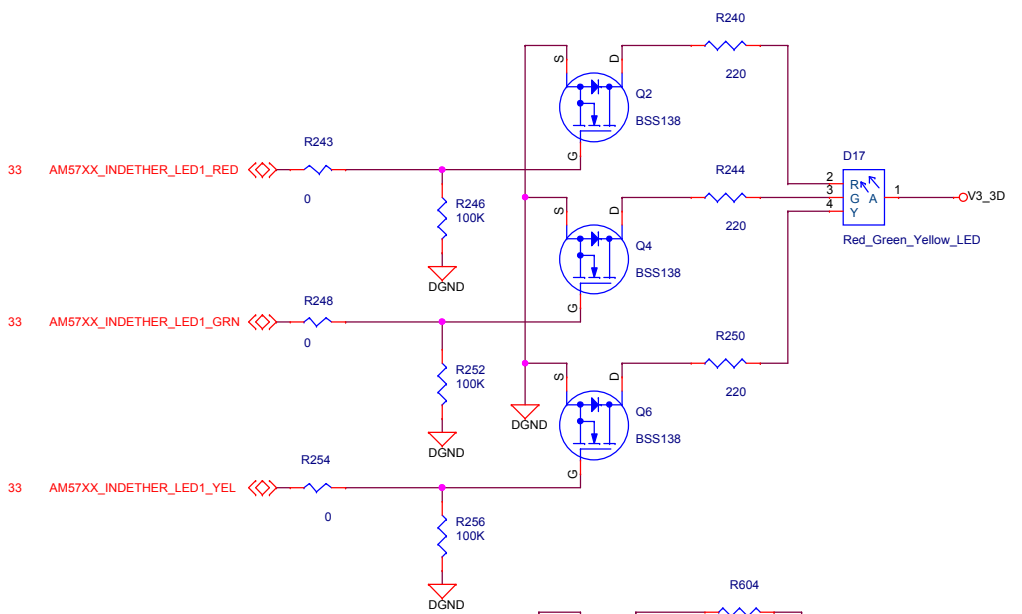
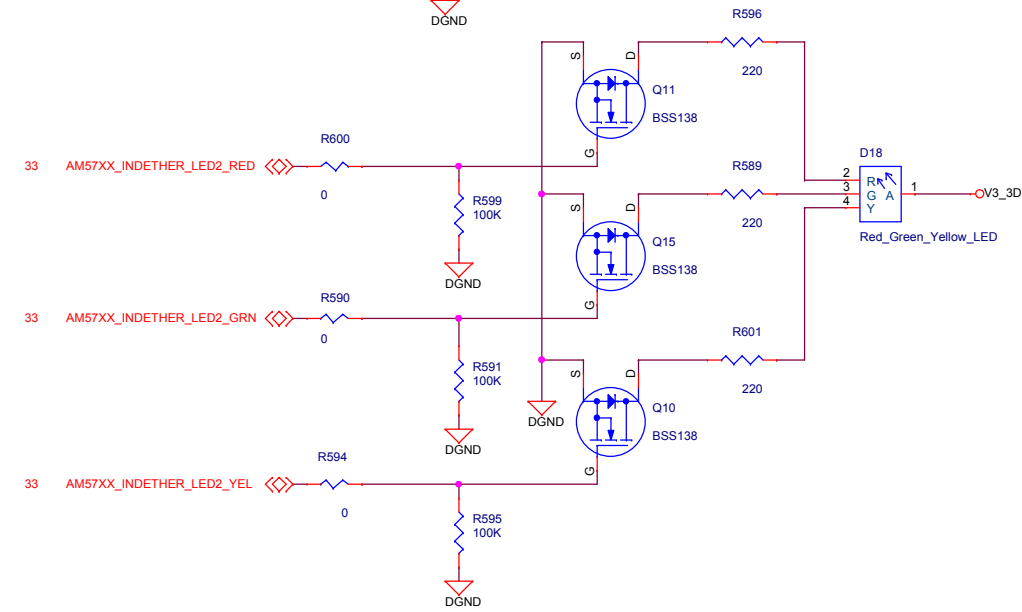
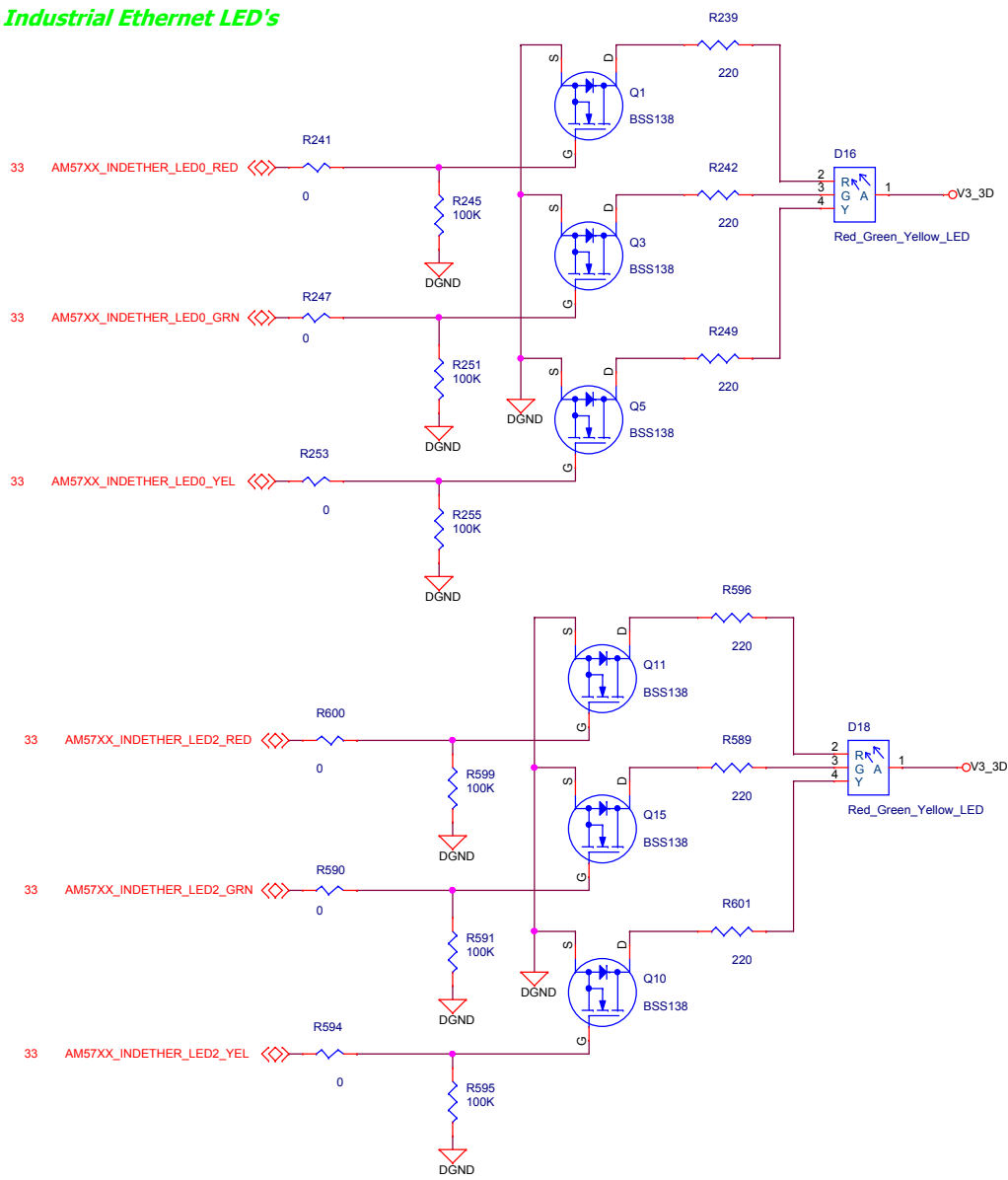
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AM572X Industrial EVM
ICSS #2 MII0

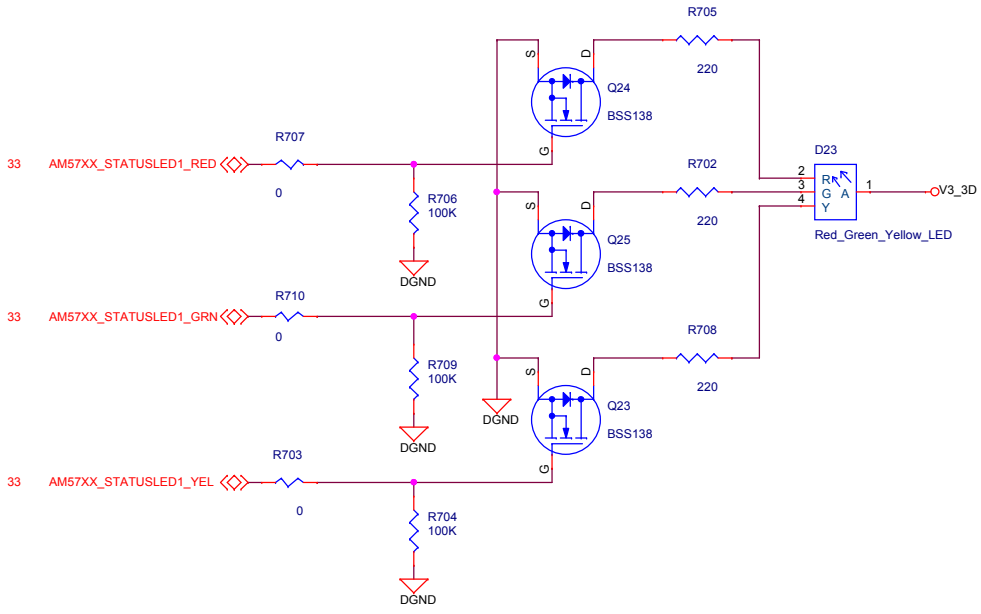
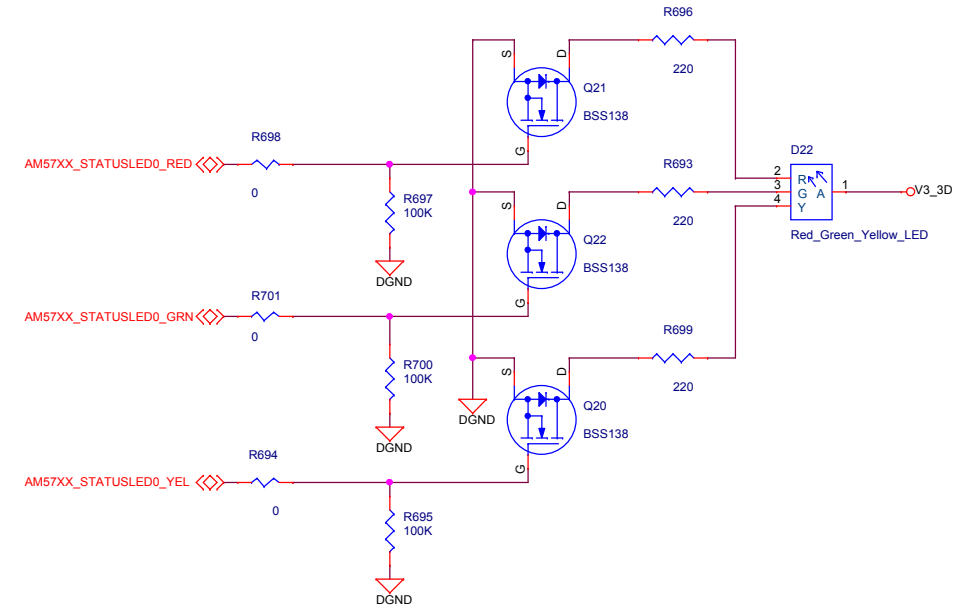
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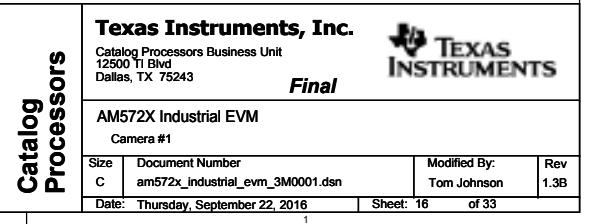


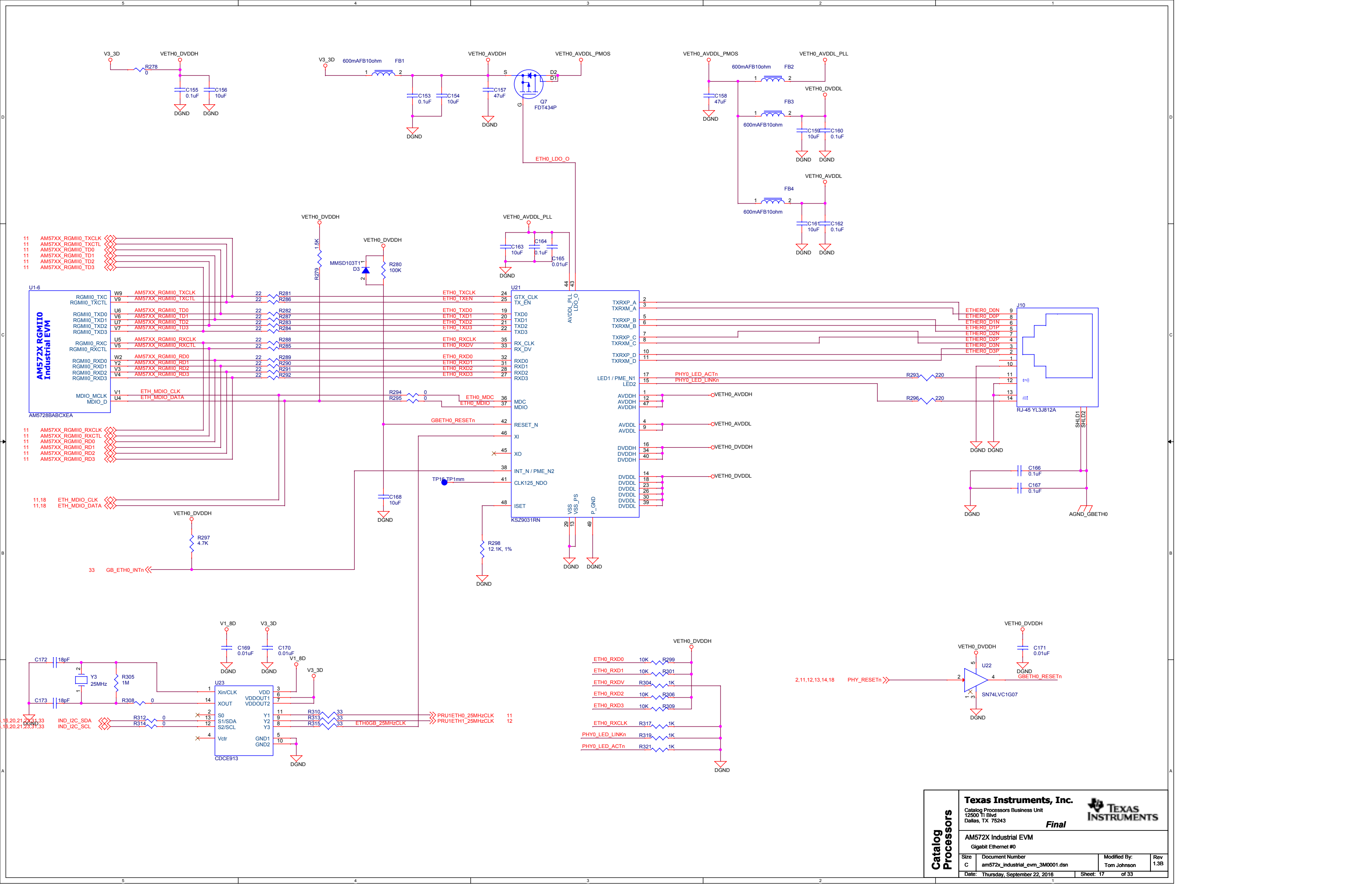
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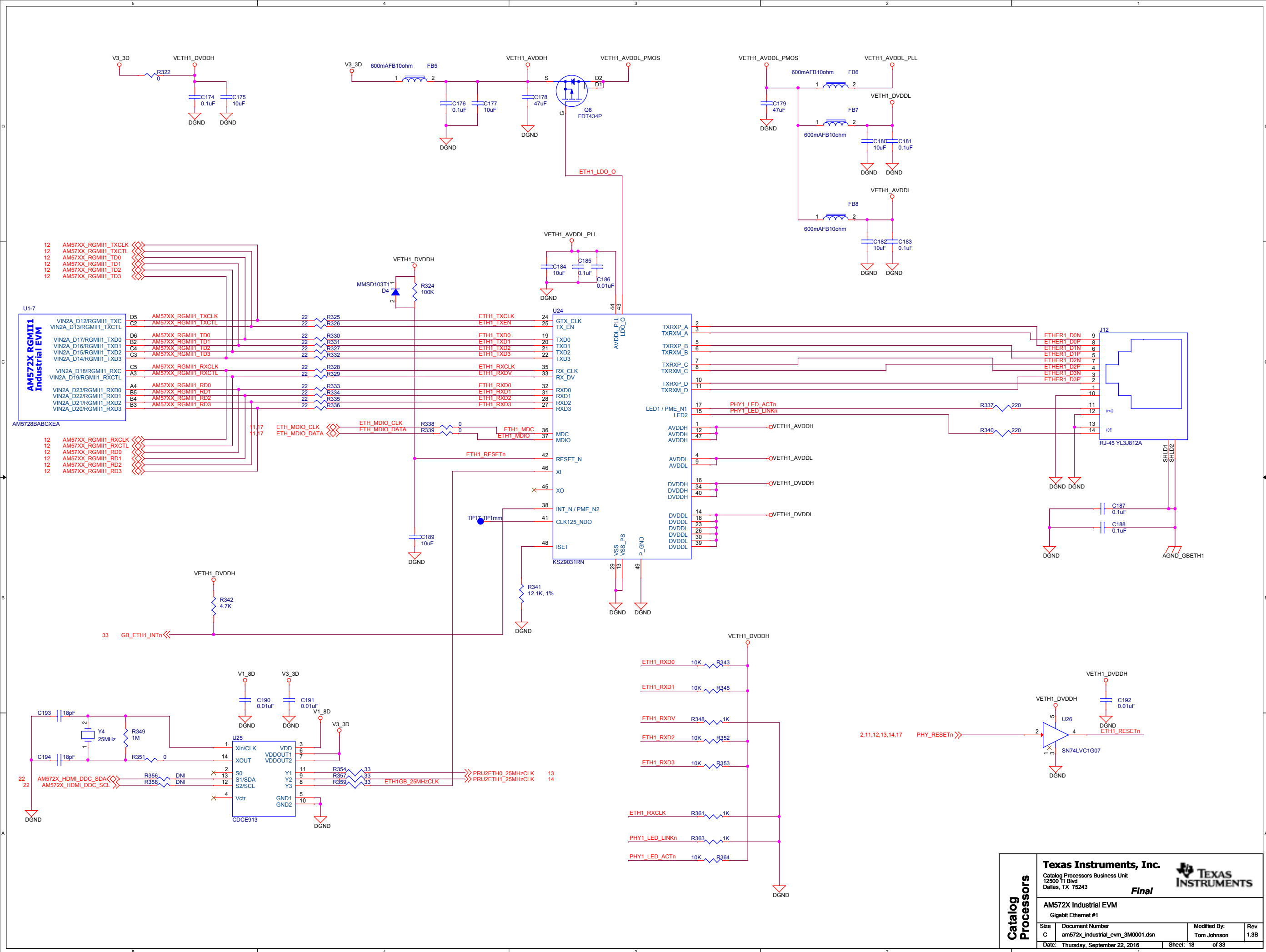


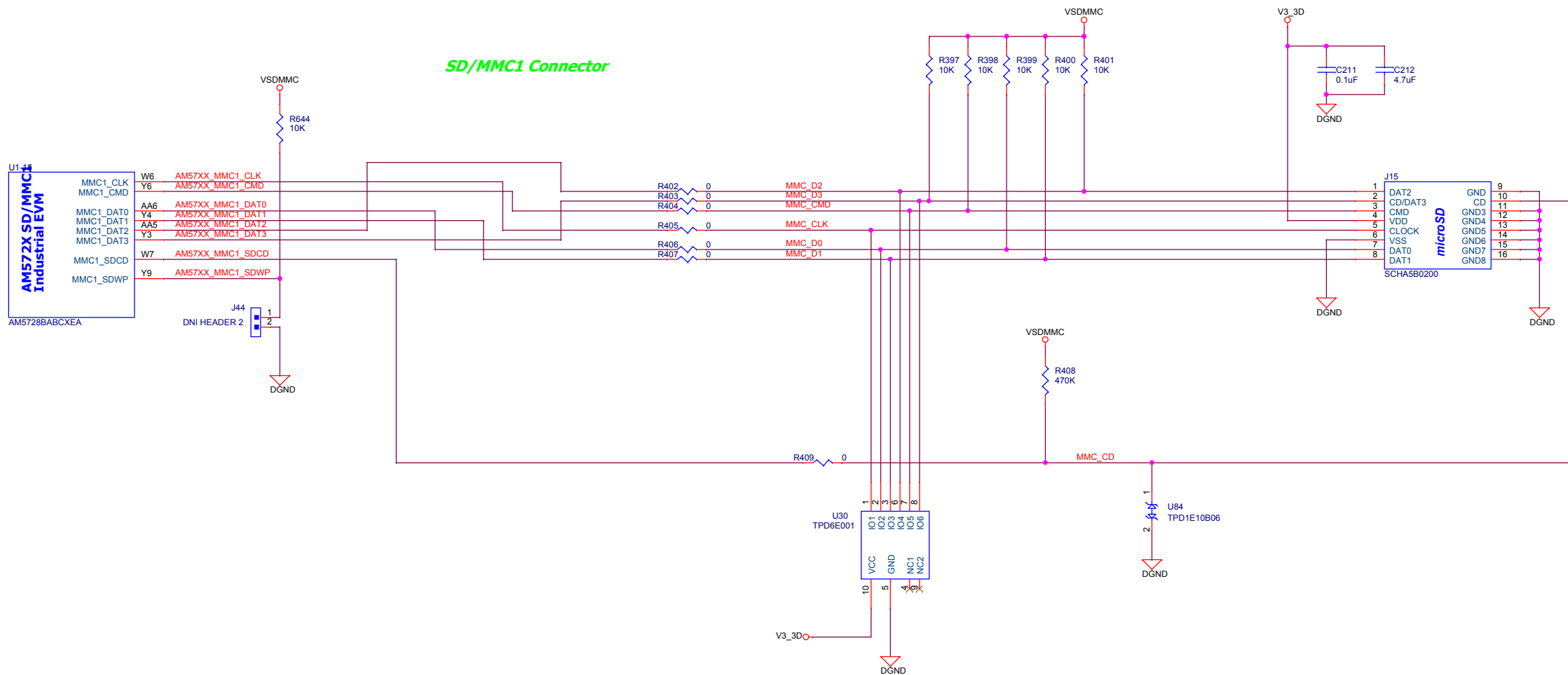
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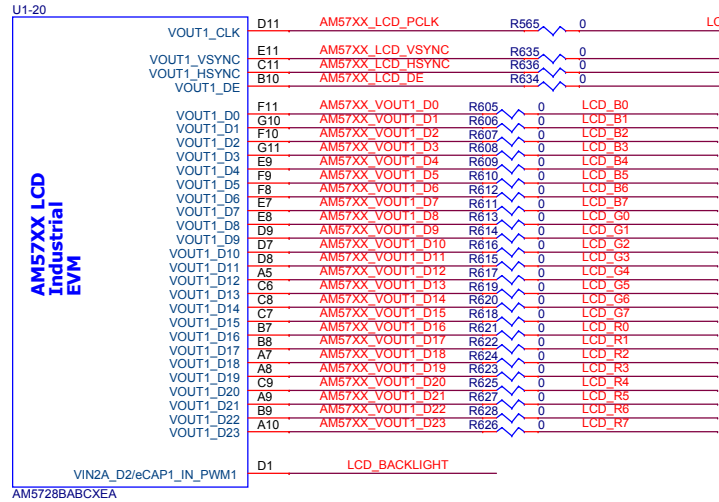












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2,7,33 SYS_RESETr <>> R566 0

5,16,17,20,21,23,31,33 IND_I2C_SCL <>> R567 0

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TP24 TP1mm

TC358778

V1_8D

VMAIN

V1_2D

C407 1uF

NR

TPS71712

OUT

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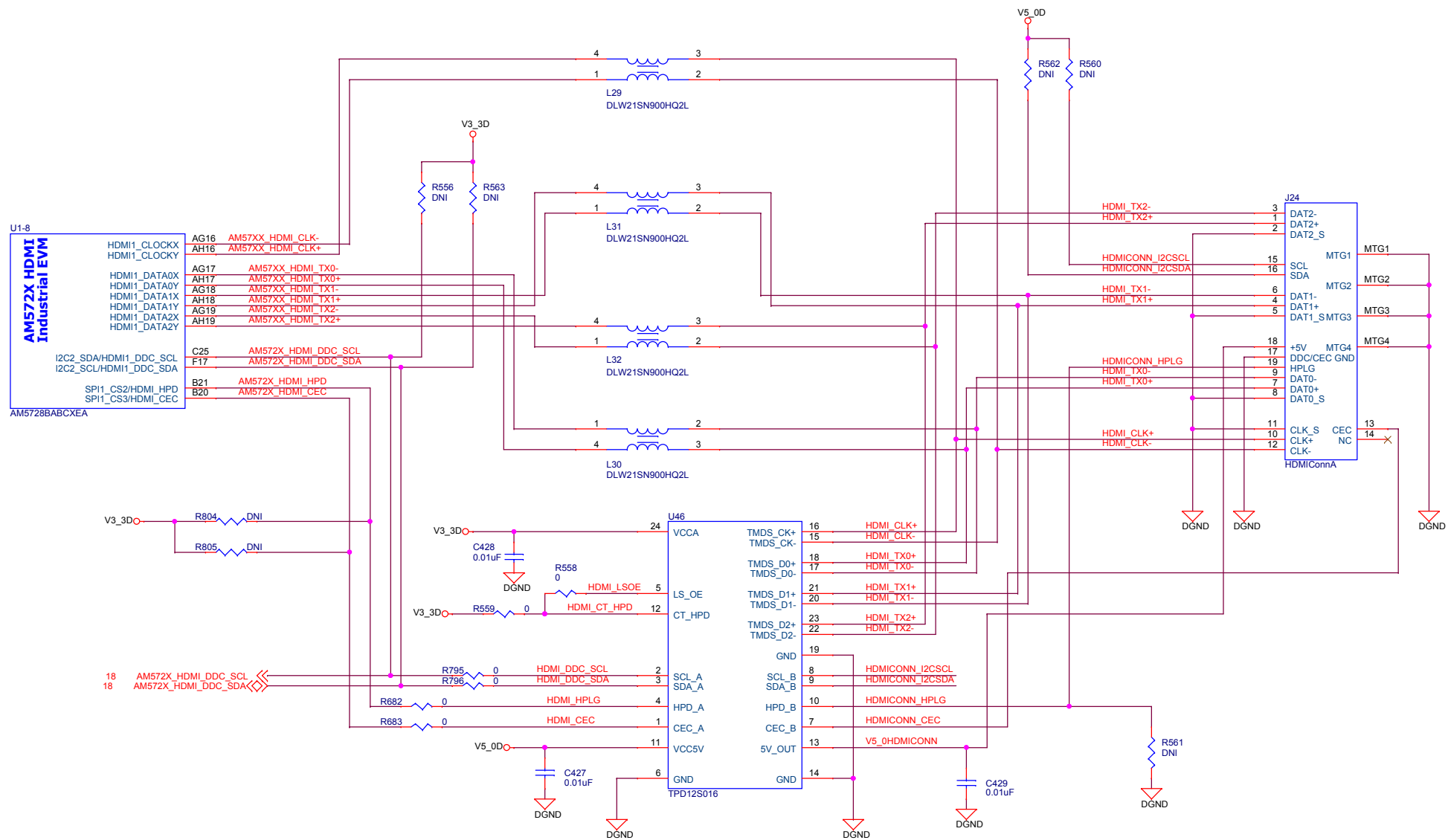
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AM572X Industrial EVM

Debug/Configuration

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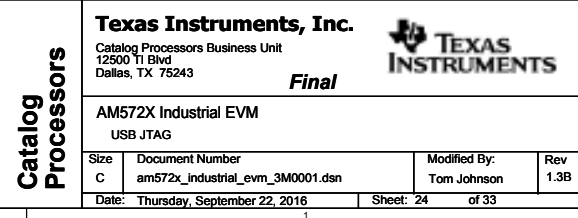
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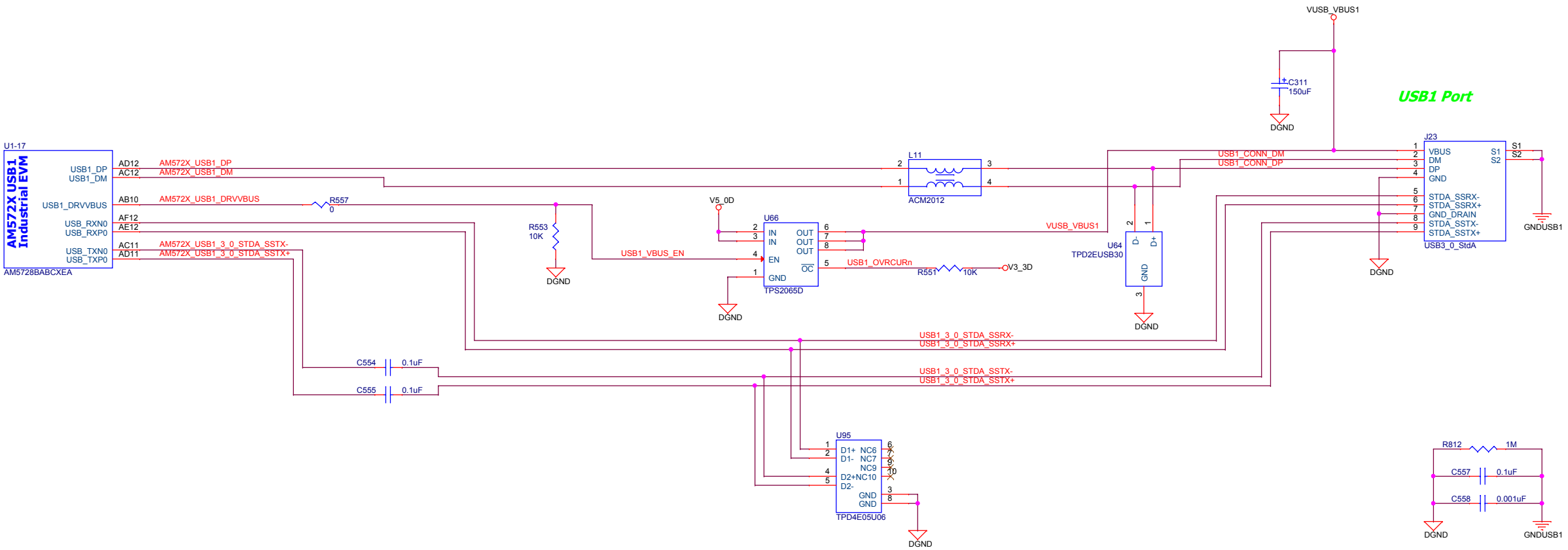
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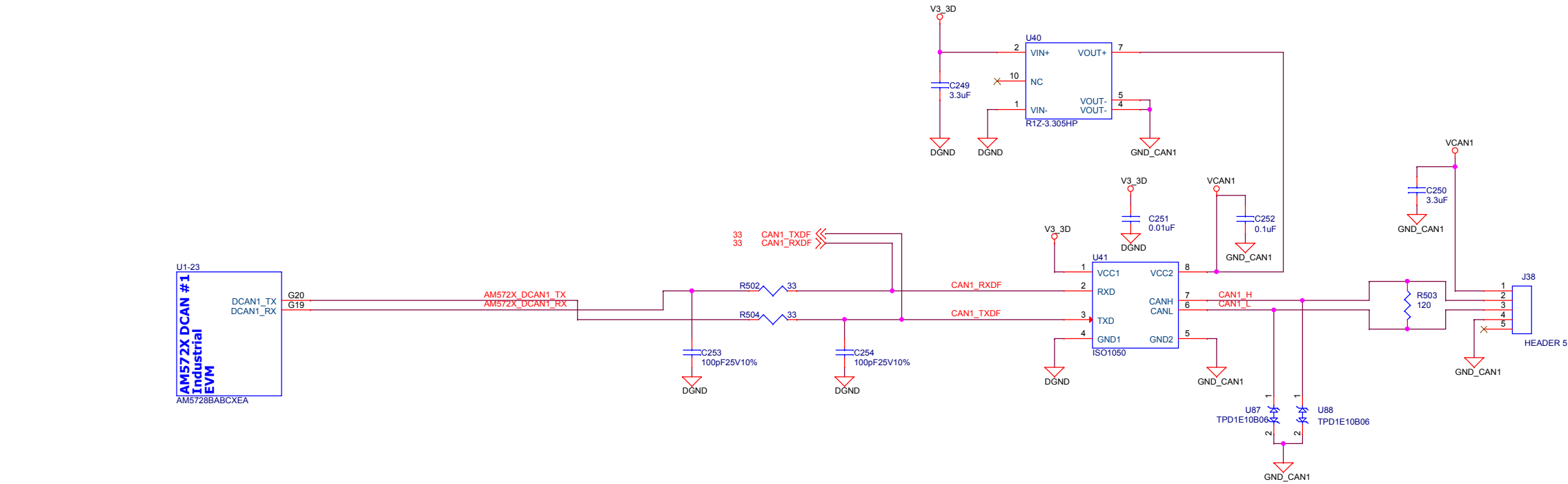
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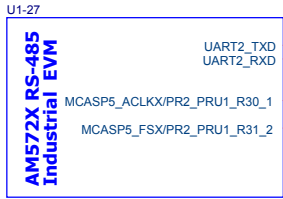
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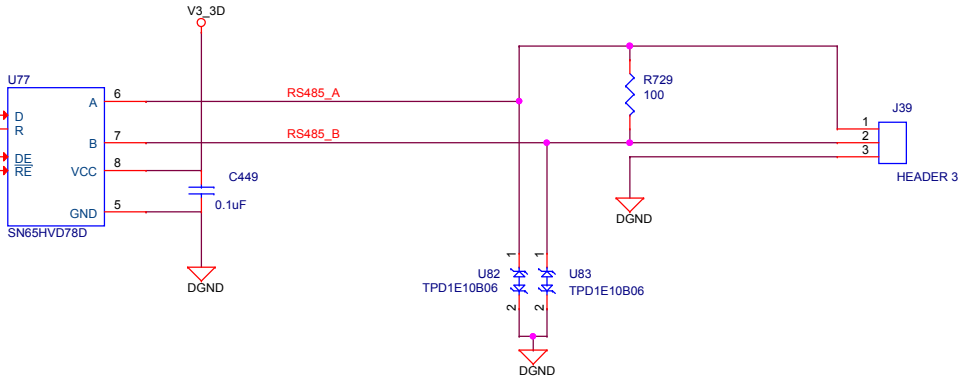




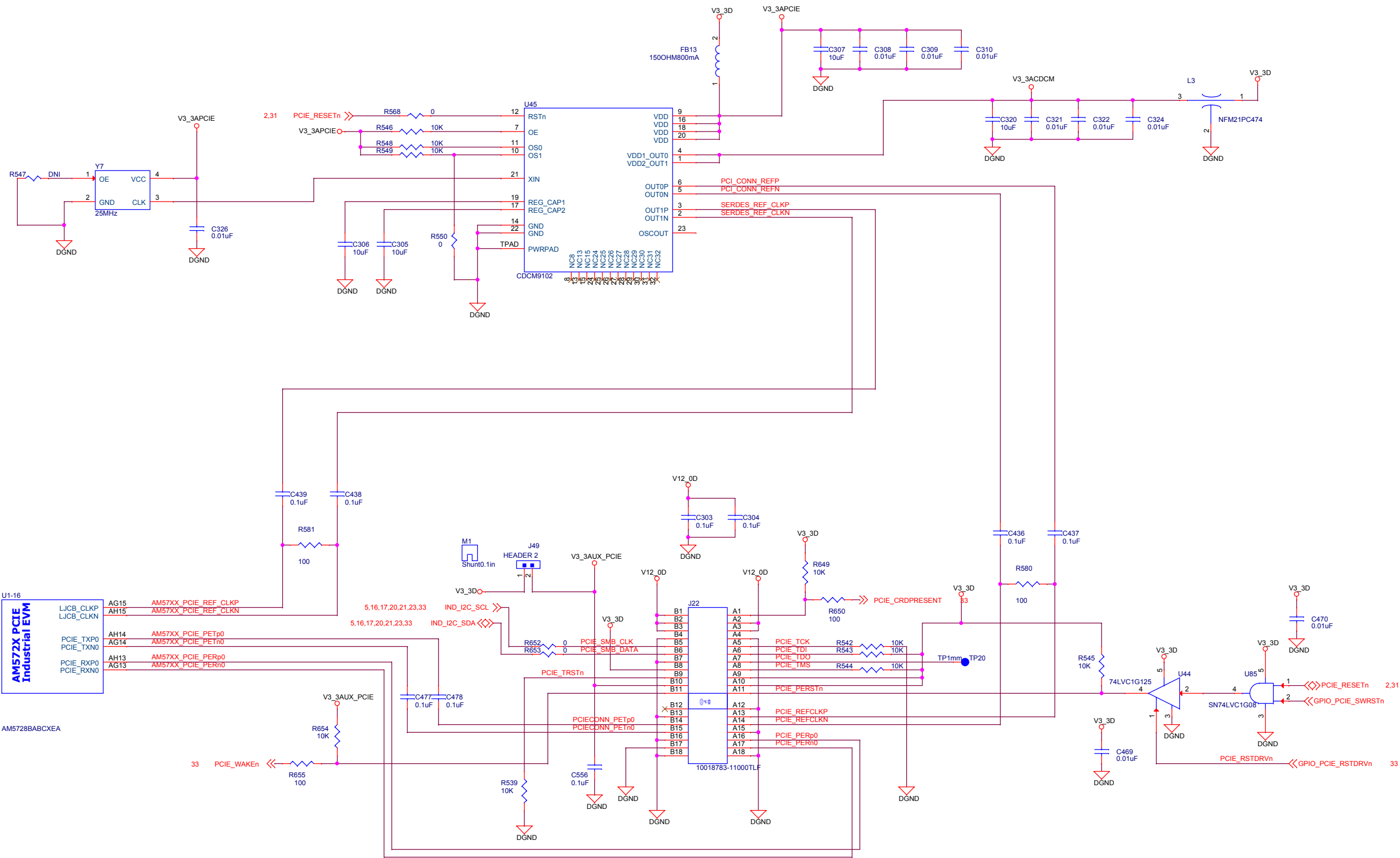


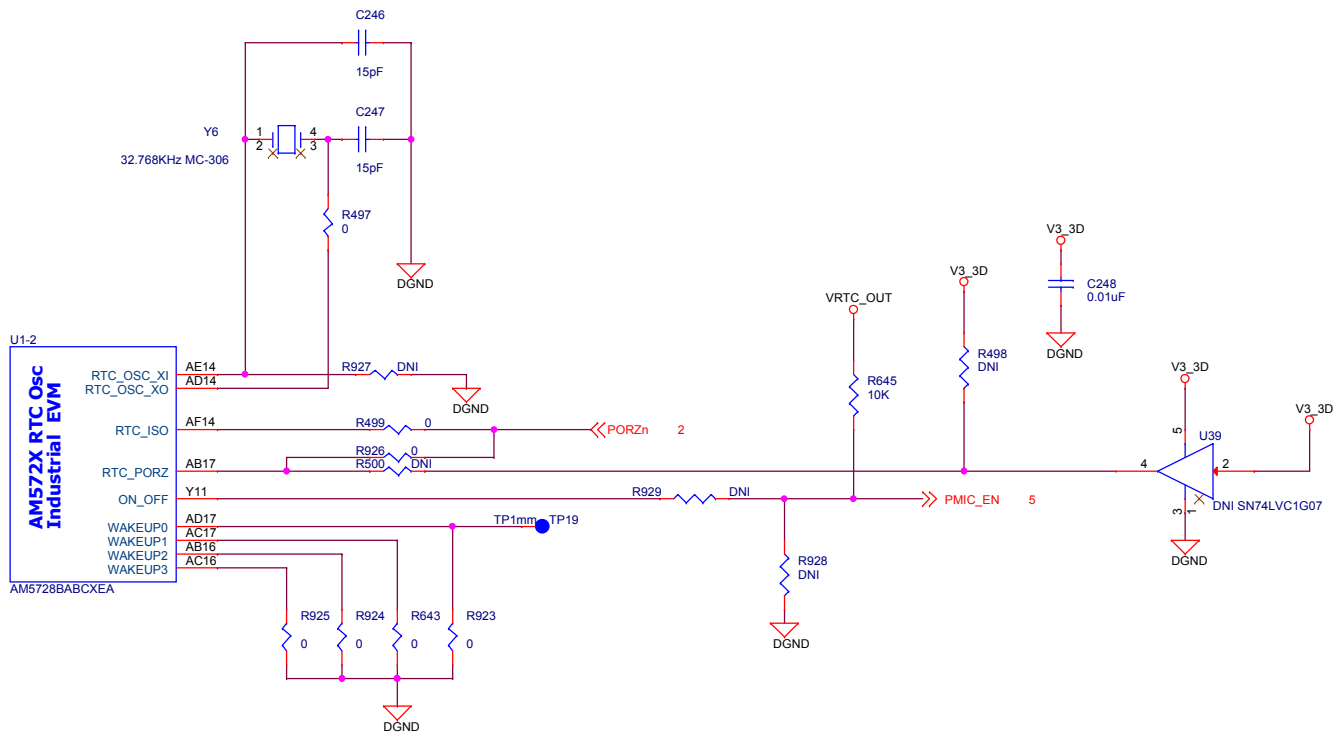


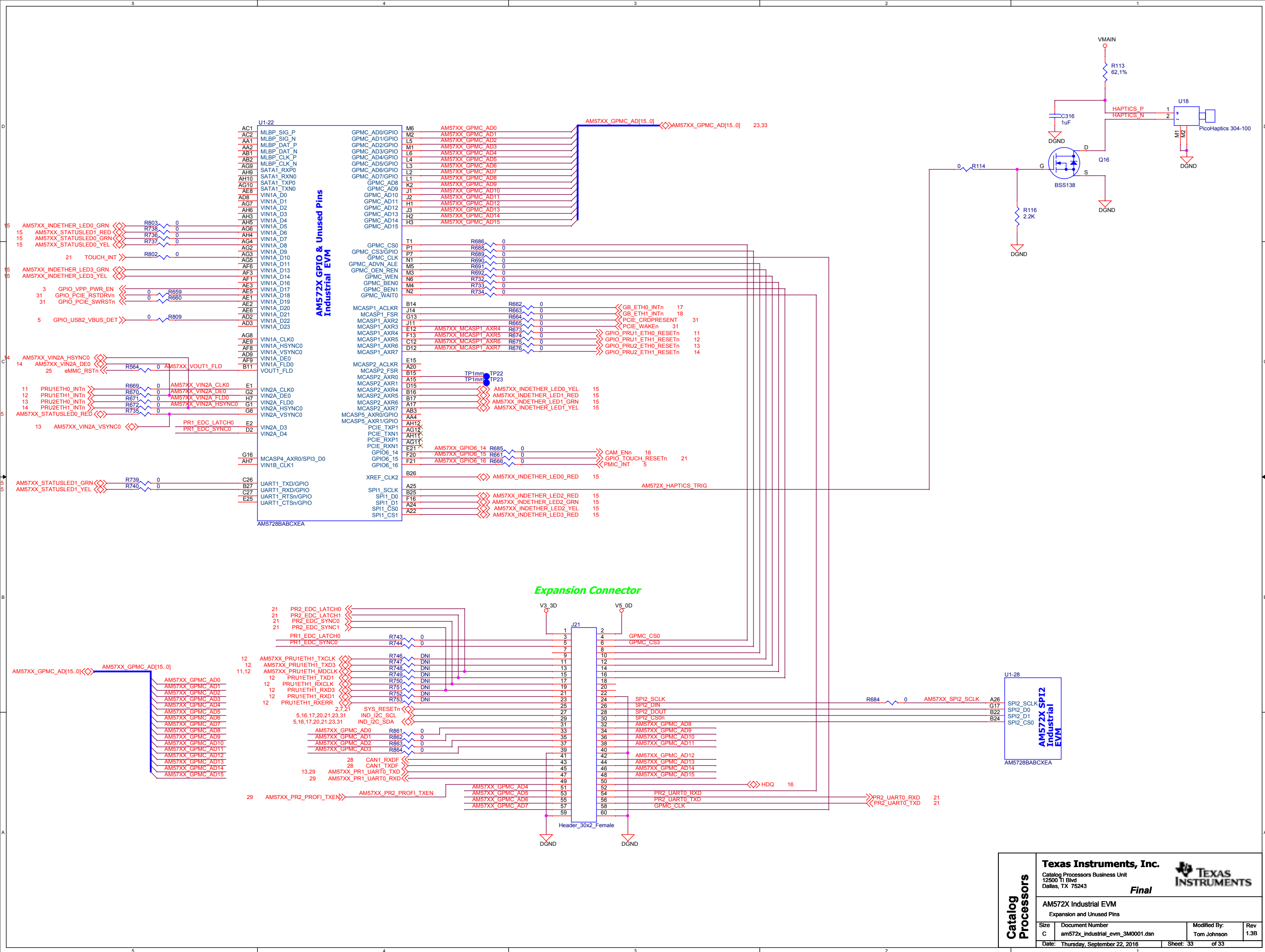
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